DS1100PED-3

1100 Watts
Distributed Power System

Total Power: 1100 Watts
Input Voltage: 90 to 264 Vac
# of Outputs: Main + Standby

Special Features

- Active Power Factor Correction
- High-power and short form factor
- 80plus Platinum Efficiency
- 1U power supply
- High-density design:24W/in^3
- Inrush current control
- EN61000-3-2 Harmonic compliance
- +12 Vdc Main Output
- +12 Vdc Standby
- Hot -Pluggable
- N+1 or N+N Redundant
- Active current sharing (20% - 100% load)
- Accurate input power reporting
- Compatible with Artesyn's Universal PMBus GUI
- Full digital control
- Two years warranty
- Reverse airflow optional

Safety

UL / cUL 60950
DEMKO + CB Report
EN60950
CE Mark
CCC
BSMI

Product Descriptions

The DS1100PED-3 power supply features a very wide 90 to 264 Vac input voltage range and employ active power factor correction to minimize input harmonic current distortion and to ensure compliance with the international EN61000-3-2 standard. The power supplies also feature active ac inrush current control, to automatically limit inrush current at turn-on to 55 A maximum.

The DS1100PED-3 can deliver up to 91.6 A from its main +12 Vdc payload output, and up to 3 A from its +12 Vdc Standby output. The form factor is 1U and may be used in single or in redundant configurations.

DS1100PED-3 has a power density of more than 24 Watts per cubic inch, and compliant 80 plus Platinum Efficiency, its efficiency will be 94% at nominal high AC line with 50 percent full load.

DS1100PED-3 is equipped with an I2C interface available with industry-standard PMBus™ communications protocol. It also contains a memory device (EEPROM) that is preprogrammed with data about the unit – including its type, serial number and date of manufacture – to facilitate replacement in the field.

Model Numbers

Standard	Output Voltage	Minimum Load	Maximum Load	Stand-By Supply	Air Flow Direction
DS1100PED-3	12.0Vdc	2A[1]	91.6A	12.0V@3A	Forward (DC Connector to Red Handle)
DS1100PED-3 -001	12.0Vdc	2A[1]	91.6A	12.0V@3A	Reverse (Blue Handle to DC Connector)

Note 1 - Minimum current for transient load response testing only. Unit is designed to operate and be within output regulation range at zero load.

Options

None

Electrical Specifications

Absolute Maximum Ratings

Stress in excess of those listed in the "Absolute Maximum Ratings" may cause permanent damage to the power supply. These are stress ratings only and functional operation of the unit is not implied at these or any other conditions above those given in the operational sections of this TRN. Exposure to any absolute maximum rated condition for extended periods may adversely affect the power supply's reliability.

Table 1. Absolute Maximum Ratings:

Parameter	Model	Symbol	Min	Typ	Max	Unit
Input Voltage: AC continuous operation	All models	$V_{IN,AC}$	90	-	264	Vac
Maximum Output Power (Main + Standby)	All models	$P_{O,max}$	-	-	1100	W
Isolation Voltage Input to outputs Input to safety ground	 All models All models		 - -	 - -	 4243 3232	 Vdc Vdc
Ambient Operating Temperature	All models	T_A	0	-	+60[1]	°C
Storage Temperature	All models	T_{STG}	-40	-	+70	°C
Humidity (non-condensing) Operating Non-operating	 All models All models		 20 10	 - -	 80 95	 % %
Altitude Operating Non-operating	 All models All models		 - -	 - -	 10,000 50,000	 feet feet

Note 1 - Operation up to 60 °C is allowed with power derating for both DS1100PED-3 and DS1100PED-3-001 power supplies (see page 24 power derating curve).

Input Specifications

Table 2. Input Specifications:

Parameter	Conditions	Symbol	Min	Typ	Max	Unit
Operating Input Voltage, AC	All	$V_{IN,AC}$	90	115/230	264	Vac
Input Vac Source Frequency	All	$f_{IN,AC}$	47	50/60	63	Hz
Maximum Input Current ($I_O = I_{O,max}$, $I_{SB} = I_{SB,Max}$)	$V_{IN,AC}$ = 90Vac	$I_{IN,max}$	-	-	14.5	A_{RMS}
Standby Input Current (V_O =Off, I_{SB} = 0A)	$V_{IN,AC}$ = 90Vac	$I_{IN,Standby}$	-	-	250	mA_{RMS}
Standby Input Power (V_O =Off, I_{SB} = 0A)	All	$P_{W,Standby}$	-	-	6	W
No Load Input Current (V_O =On, I_O = 0A, I_{SB} = 0A)	$V_{IN,AC}$ = 90Vac	I_{IN,no_load}	-	-	300	mA_{RMS}
Harmonic Line Currents	All	THD	Per IEC1000-3-2			
Power Factor	With 20% load and above		0.9	-	-	
Startup Surge Current (Inrush) @ 25°C	$V_{IN,AC}$ = 264Vac	$I_{IN,surge}$	-	-	55	A_{PK}
Input Fuse	Internal,5x20mm, Quick Acting 16A, 250V		-	-	16	A
Leakage Current to earth ground	$V_{IN,AC}$ = 240V_{AC} $f_{IN,AC}$ = 50/60 Hz		-	-	1.40	mA
Operating Efficiency @ 25°C	I_O = 50%$I_{O,max}$ $V_{IN,AC}$ = 230Vac	η	-	-	94	%
System Stability: Phase Margin Gain Margin	 All All		 45 -10	 - -	 - -	 Ø dB

Output Specifications

Table 3. Output Specifications:

Parameter		Condition	Symbol	Min	Typ	Max	Unit
Output Regulation	All models	Inclusive of set-point, temperature change, warm-up drift and dynamic load	V_O	11.4	12.0	12.6	V
	All models		V_{SB}	11.4	12.0	12.6	
Output Ripple, pk-pk	All models	Measure with a 0.1uF ceramic capacitor in parallel with a 10uF tantalum capacitor, 0 to 20MHz bandwidth	V_O	-	-	120	mV_{PK-PK}
	All models		V_{SB}	-	-	120	
Output Current	All models	$90 \leq V_{IAC} \leq 264Vac$	I_O	2[1]	-	91.6	A
	All models		I_{SB}	0.1[1]	-	3.0	
Vo Current Share Accuracy		20% to 100% I_O		-	-	3.7	A
V_O Minimum Current Share Loading				10	-	-	$\%I_{O,max}$
Number of Parallel Units[2]		Main Output Current Share connected		-	-	6	
V_O Load Capacitance		Start up	C_O	2000	-	40000	µF
			C_{SB}	47	-	4700	
V_O Dynamic Response Peak Deviation		50% load change, slew rate = 1A/µs	$\%V_O$	-5	-	5	%
V_O Long Term Stability Max change over 24 hours		After thermal equilibrium (30 mins)	$\%V_O$	-0.2	-	0.2	%

Note 1 - Minimum current for transient load response testing only. Unit is designed to operate and be within output regulation range at zero load.
Note 2 - Vsb output do not use active current sharing. On paralleled units, maximum current on Vsb output rail should not exceed the current of one unit.

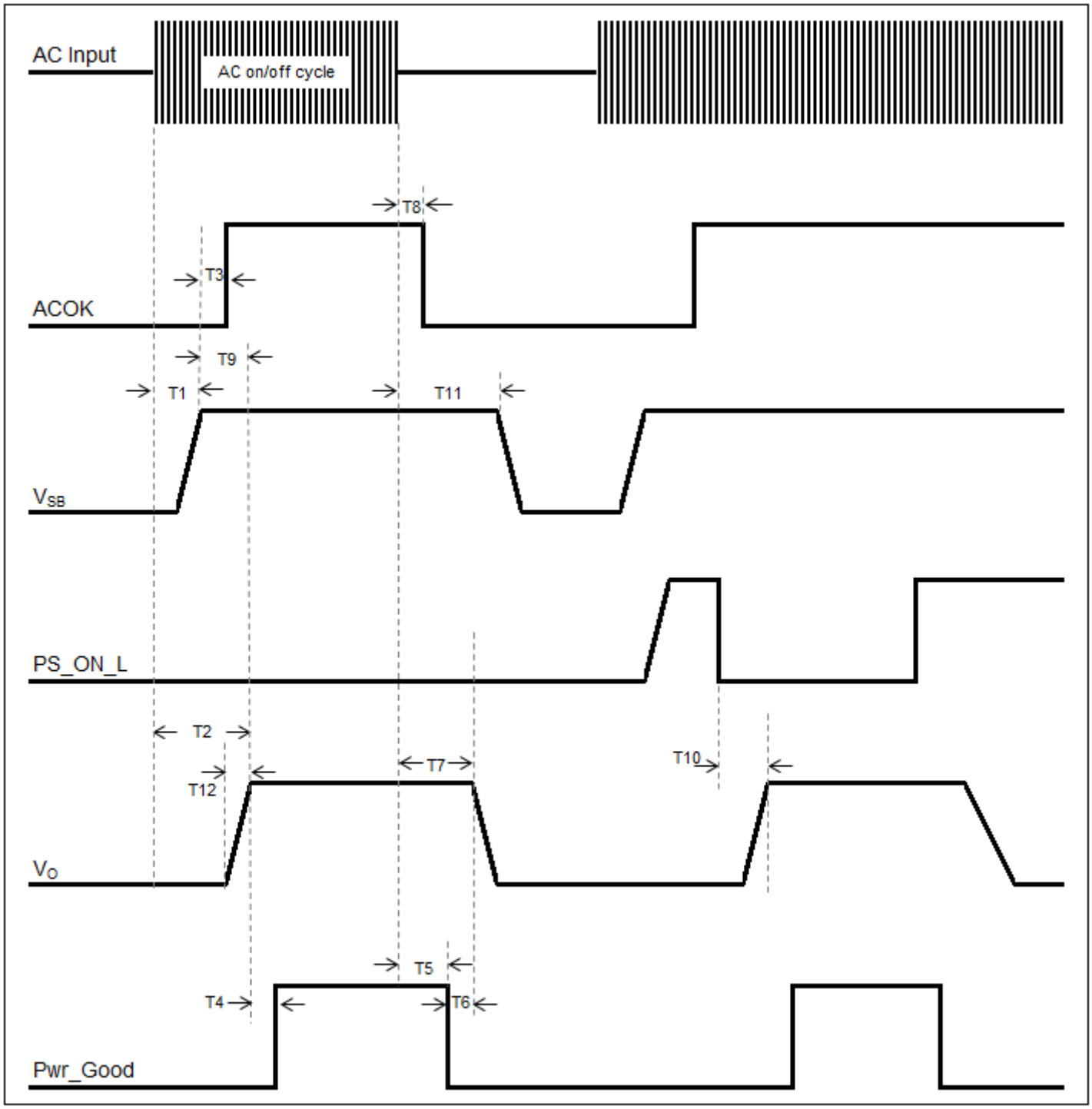
System Timing Specifications

Table 4. System Timing Specifications:

Label	Parameter	Min	Typ	Max	Unit
T1	Delay from AC being applied to V_{SB} being within regulation.	20	-	1700	mSec
T2	Delay from AC being applied to main output voltages being within regulation.	-	-	2200	mSec
T3	Delay from Standby output to ACOK assertion	-	-	20	mSec
T4	Delay from output voltages within regulation limits to Pwr_Good asserted.	100	-	1000	mSec
T5	Delay from loss of AC to deassertion of Pwr_Good.	10	-	-	mSec
T6	Delay from deassertion of Pwr_Good to output voltages falling out of regulation.	1	-	-	mSec
T7	Delay from loss of AC to main output being within regulation.	11	-	-	mSec
T8	Delay from loss of AC to assertion of ACOK	-	-	6	mSec
T9	Delay from Standby output to main output voltage being within regulation.	-	-	300	mSec
T10	Delay from PS_ON_L assertion to output voltages being within regulation.	-	-	350	mSec
T11	Delay from loss of AC to Standby output being within regulation.	150	-	-	mSec
T12	Output voltage rise time from the main output.	5	-	50	mSec

System Timing Specifications

Figure 1. System Timing Diagram:



DS1100PED-3 Performance Curves

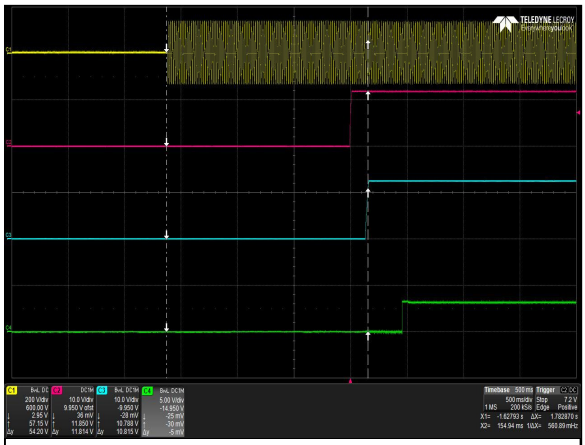


Figure 1: DS1100PED-3 Turn-on delay via AC mains – Vin = 90Vac
Full Load: Io = 91.6A, Isb = 3A
Ch 1: AC Mains Ch 2: Vsb Ch 3: Vo Ch 4: Pwr_Good

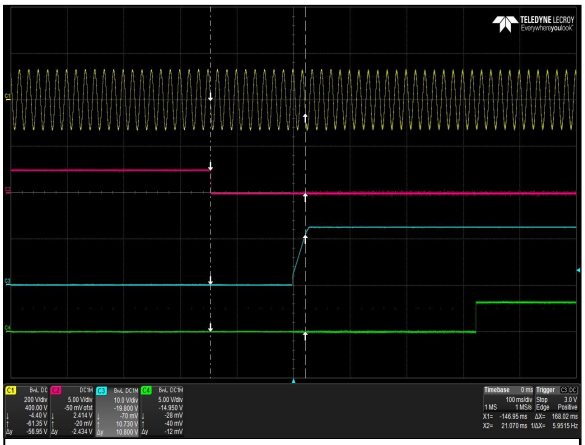


Figure 2: DS1100PED-3 Turn-on delay via PS_ON_L – Vin = 90Vac
Full Load: Io = 91.6A, Isb = 3A
Ch 1: AC Mains Ch 2: PS_ON_L Ch 3: Vo Ch 4: Pwr_Good

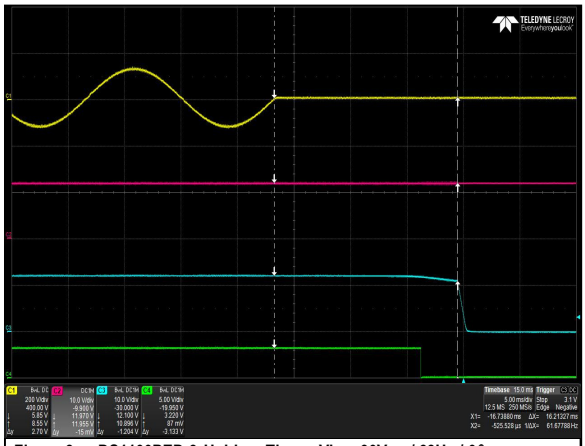


Figure 3: DS1100PED-3 Hold-up Time – Vin = 90Vac / 63Hz / 0°
Full Load: Io = 91.6A, Isb = 3A
Ch 1: AC Mains Ch 2: Vsb Ch 3: Vo Ch 4: Pwr_Good

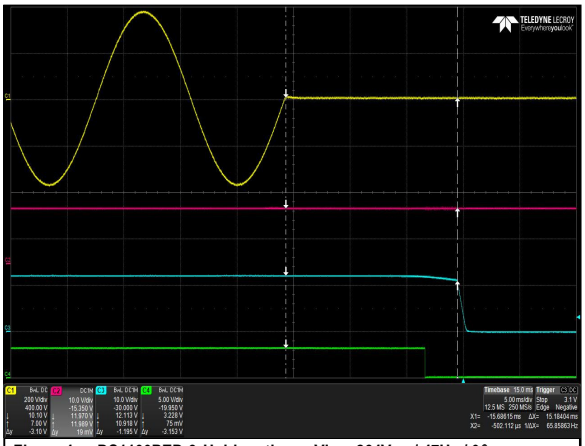


Figure 4: DS1100PED-3 Hold-up time – Vin = 264Vac / 47Hz / 0°
Full Load: Io = 91.6A, Isb = 3A
Ch 1: AC Mains Ch 2: Vsb Ch 3: Vo Ch 4: Pwr_Good

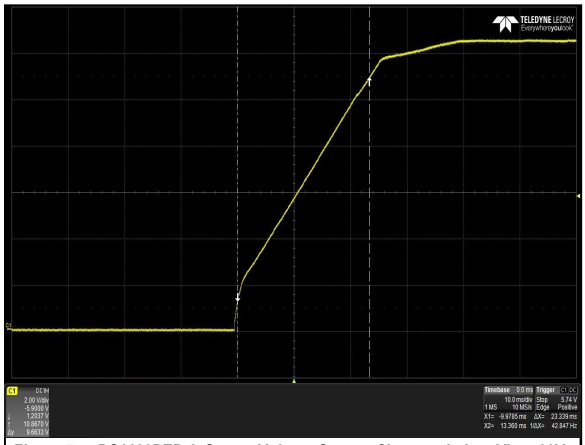


Figure 5: DS1100PED-3 Output Voltage Startup Characteristic – Vin = 90Vac
Full Load: Io = 91.6A, Isb = 3A
Ch 1: Vo

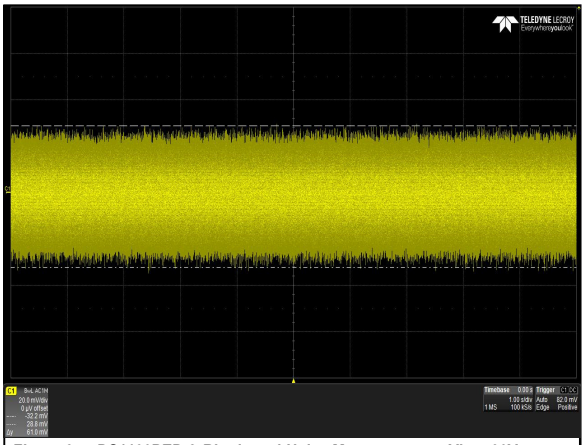


Figure 6: DS1100PED-3 Ripple and Noise Measurement – Vin = 90Vac
Full Load: Io = 91.6A, Isb = 3A
Ch 1: Vo

DS1100PED-3 Performance Curves

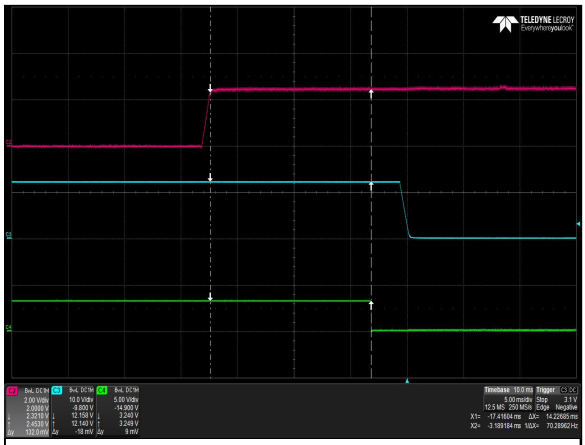


Figure 7: DS1100PED-3 Turn Off Characteristic via PS_ON_L
Full Load: Io = 91.6A, Isb = 3A
Ch 2: PS_ON_L Ch 3: Vo Ch 4: Pwr_Good

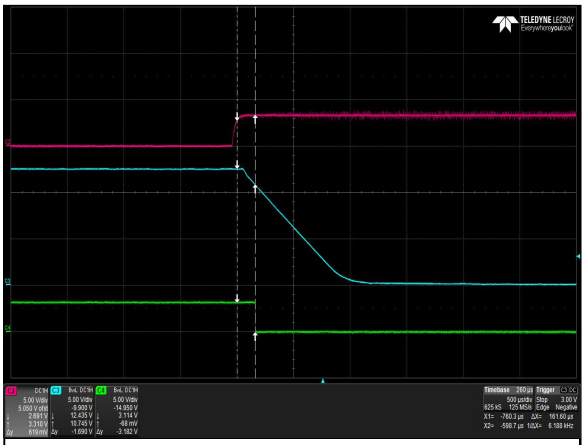


Figure 8: DS1100PED-3 Turn Off Characteristic via PS_KILL_L
Full Load: Io = 91.6A, Isb= 3A
Ch 2: PS_KILL_L Ch 3: Vo Ch 4: Pwr_Good

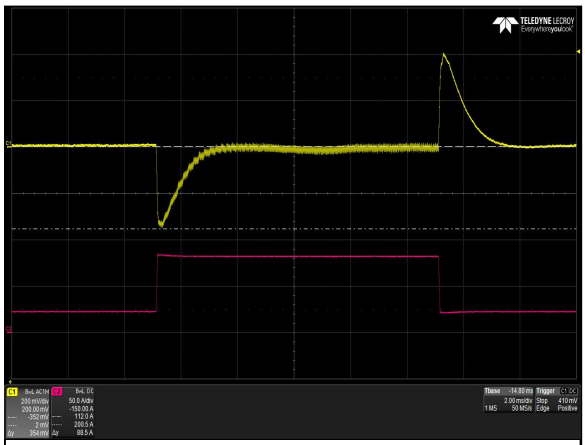


Figure 9: DS1100PED-3 Transient Response – Vo Deviation (low to high)
25% to 75% load change, 1A/µS slew rate, Vin = 230Vac, with 4700uF Cap.
Ch 1: Vo Ch 2: Io

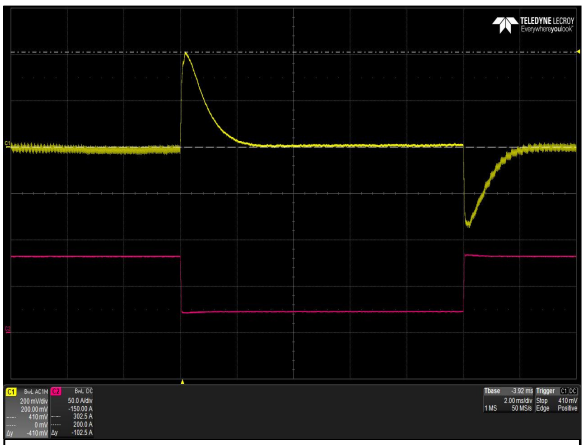


Figure 10: DS1100PED-3 Transient Response – Vo Deviation (high to low)
75% to 25% load change, 1A/µS slew rate, Vin = 230Vac, with 4700uF Cap.
Ch 1: Vo Ch 2: Io

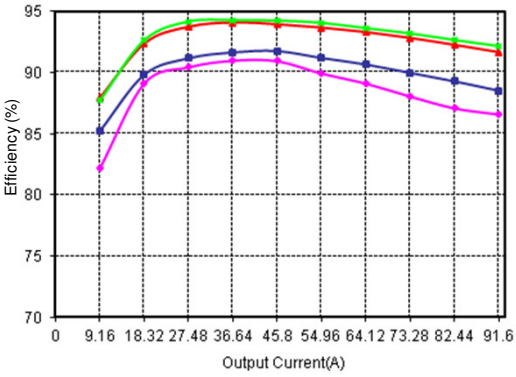


Figure 11: DS1100PED-3 Efficiency Curves @ 25 degC
90 Vac 115 Vac 230 Vac 264 Vac
Loading: Vo = 10% increment to 91.6A, Isb = 3A

Protection Function Specification

Input Fusing

DS1100PED-3 series is equipped with an internal non user serviceable 16A Fast Acting 250 Vac fuse to IEC 127 for fault protection in the L line input.

Over Voltage / Under Voltage Protection (OVP / UVP)

The power supply will provide latch mode over and under voltage protection as defined by the output under voltage and output over voltage parameters for each output.

OVP

Parameter	Min	Nom	Max	Unit
V_O Output Overvoltage	13.5	/	15.0	V
Standby Overvoltage	13.5	/	15.0	V

UVP

Parameter	Min	Nom	Max	Unit
V_O Output Undervoltage	10.5	/	11.0	V
Standby Undervoltage	10.0	/	11.0	V

Over Current Protection (OCP)

DS1100PED-3 series includes internal current limit circuitry to prevent damage in the event of overload or short circuit. Recovery is automatic when the overload is removed, if the overload lasts for 500 millisecond or less, and if it is less than or equal to 120% of rated load. If the overload is > 125% of rated load, the power supply will latch off immediately within 10ms. If the overload is between 120% and 125% of rated load, the power supply will latch off within 500ms. The latched state will require AC power / PS_ON_L recycling to restart the power supply. A fault in the main output will not cause the Standby output to shut down. No damage will result to the supply as the result of either short term or long term overloads of the outputs.

Parameter	Min	Nom	Max	Unit
V_O Output Overcurrent	108	/	138	A
Standby Overcurrent	3.6	/	4.5	A

Short Circuit Protection (SCP)

The DS1100PED-3 power supply will withstand a continuous short circuit with no permanent damage, applied to its main output during start-up or while running. A short circuit is defined as an impedance of 0.1 ohms or less.

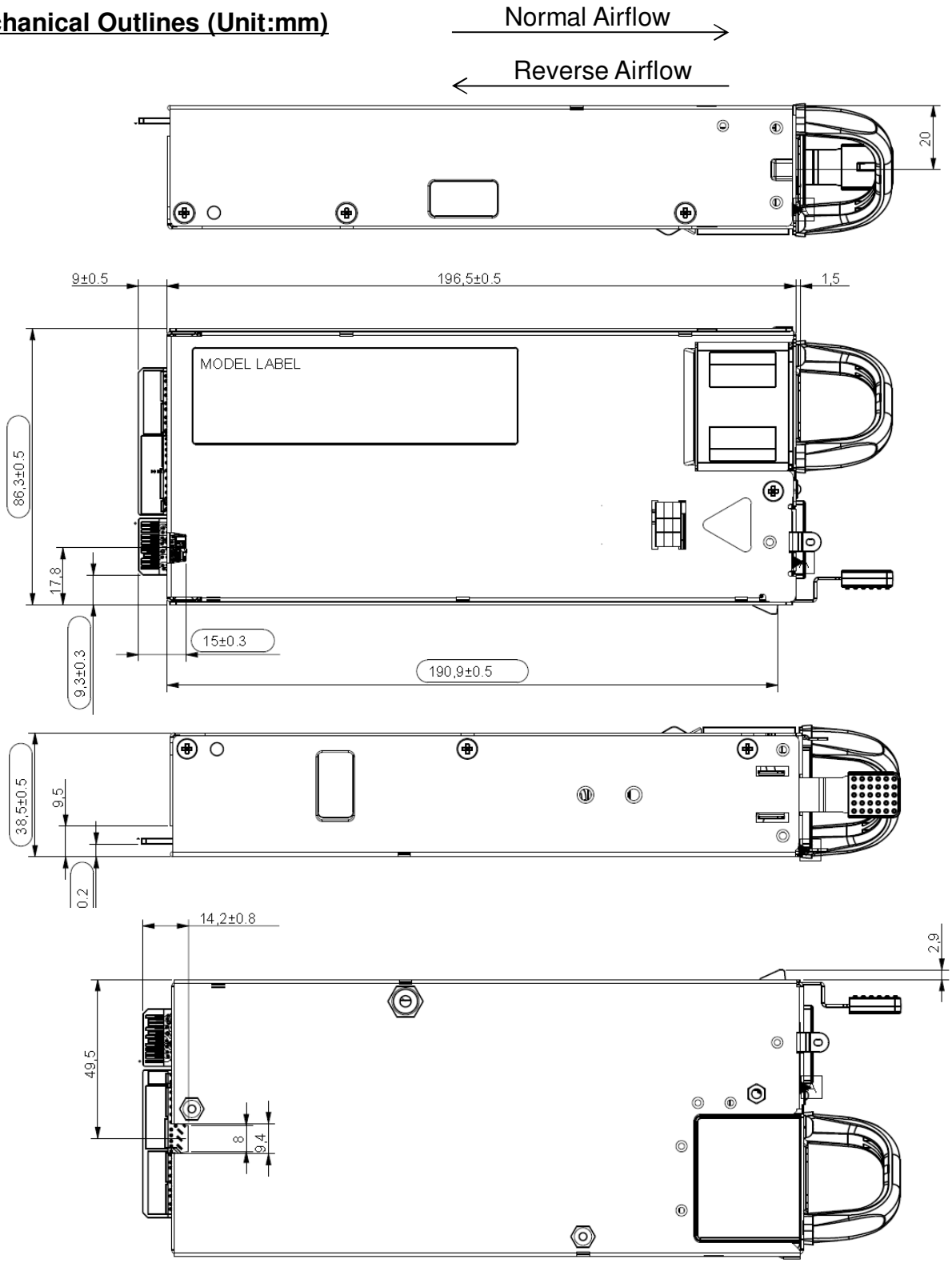
When the Standby output is shorted the output will go into “hiccup mode”. When the Standby output attempts to restart, the maximum peak current from the Standby output will be less than 20.0A peak. The maximum average current, taking into account the “hiccup” duty cycle, is less than 3.0A.

Over Temperature Protection (OTP)

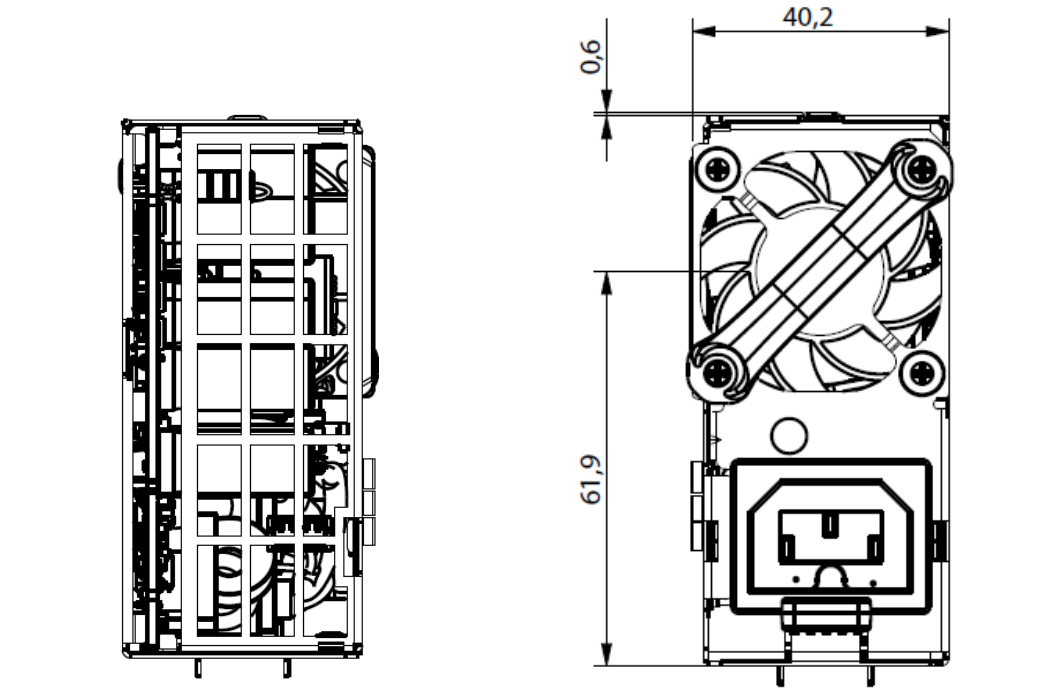
The DS1100PED-3 is internally protected against over temperature conditions. When the OTP circuit is activated, the power supply will not be damaged and main outputs will automatically restart after the over temp condition no longer exists. Hysteresis is employed to prevent a frequent toggling on and off of the outputs. The low limit point is within operating temperature range.

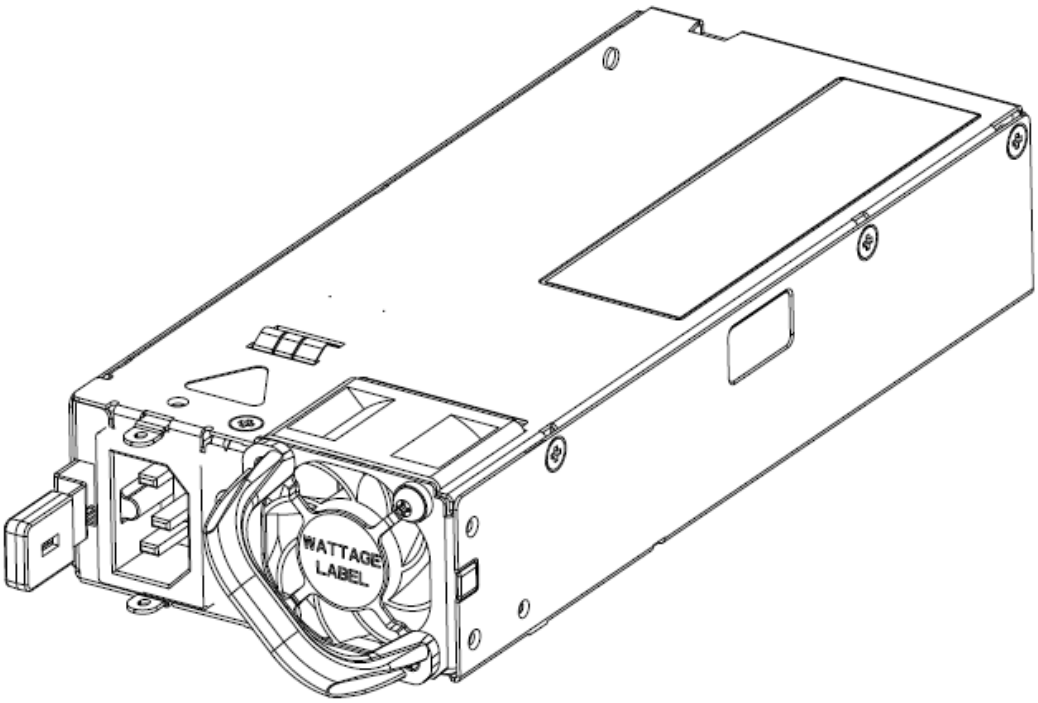
Mechanical Specifications

Mechanical Outlines (Unit:mm)

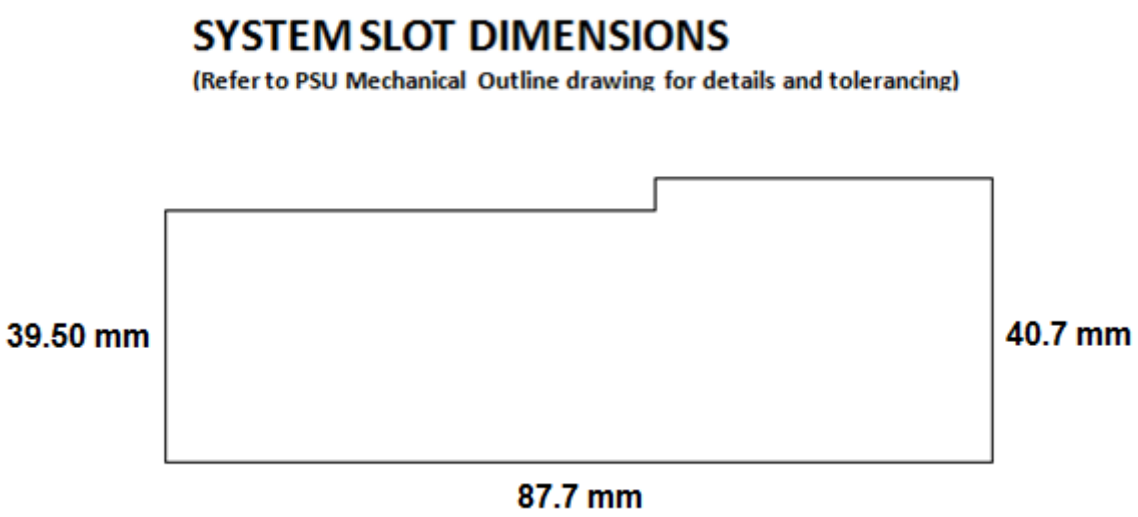


Mechanical Outlines (Unit:mm)





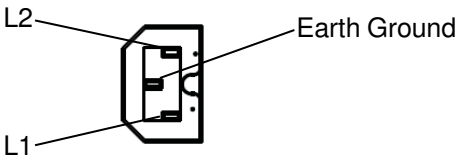
System Slot Dimensions



Connector Definitions

AC Input Connector (IEC320 C-16)

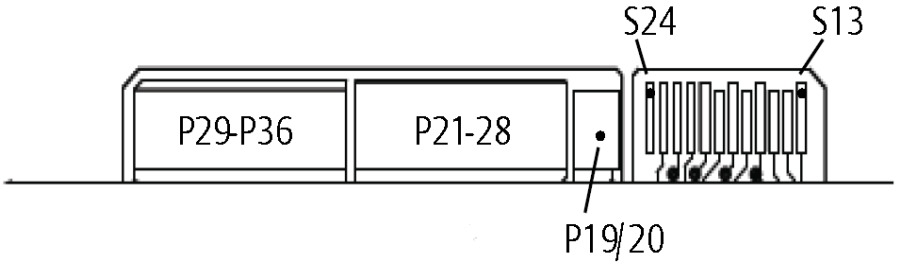
Pin 1 – L1
Pin 2 – L2
Pin 3 – Earth Ground



Output Connector – Power Blades

P1-P8 – + Main Output (V_O)
P9-P18 – Main Output Return
P19-P20 – Standby Output (Vsb)
P21-P28 – Main Output / Standby Return
P29-P36 – + Main Output (V_O)

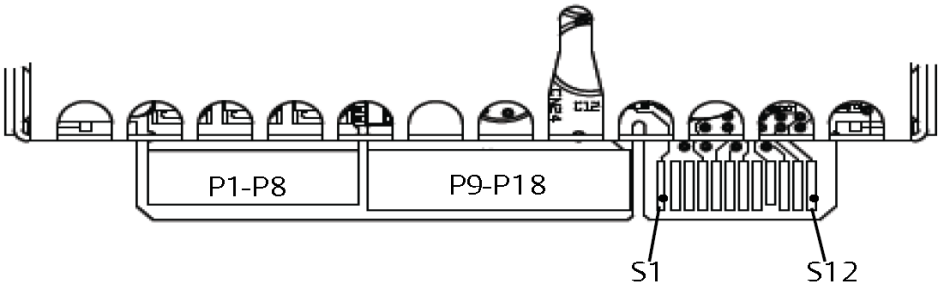
Power Supply Output Card Edge (Bottom Side)



Output Connector – Control Signals

S1 – PS PRESENT
S2 – Reserved
S3 – Reserved
S4 – Pwr_Good
S5 – ACOK (AC Input Present)
S6 – RETURN
S7 – ISHARE
S8 – Reserved
S9 – PS INTERRUPT_L
S10 – RETURN
S11 – Reserved
S12 – Reserved
S13 – PS_ON_L
S14 – PS_KILL_L
S15 – Reserved
S16 – RETURN
S17 – SDA
S18 – RETURN
S19 – SCL
S20 – RETURN
S21 – REMOTE SENSE-
S22 – RETURN
S23 – REMOTE SENSE+
S24 – Reserved

Power Supply Output Card Edge (Top Side)

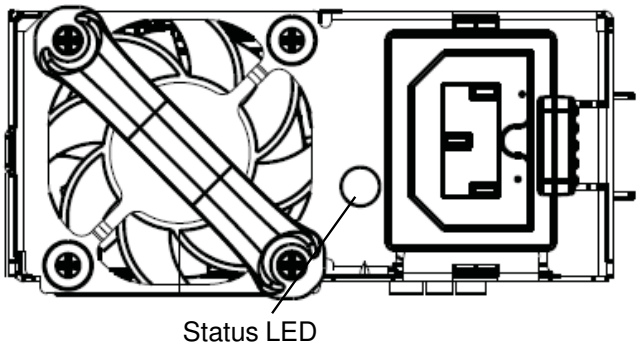


Power / Signal Mating Connectors and Pin Types

Table 5. Mating Connectors for DS1100PED-3 series

Reference	On Power Supply	Mating Connector or Equivalent
AC Input Connector	IEC320-C16	IEC320-C15
Output Connector	Card-edge	FCI Power Blade 10107844-002LF Straight Pins
		FCI Power Blade 10115859-004LF Right Angle Pins

LED indicator Definition



One bi-color (green/amber) LED at the power supply front provides status signal. The status LED conditions is shown on the below table.

Condition	LED Status
V_{SB} = ON, V_O = OFF, AC Input = ON	Blinking Amber
V_{SB} = ON, V_O = ON	Solid Green
V_O = OCP / OVP / OTP / FAN FAULT	Blinking Amber
V_{SB} = OCP	Blinking Green

Weight

The DS1100PED-3 series weight is 2.414 lbs / 1.095 kg maximum.

Environmental Specifications

EMC Immunity

DS1100PED-3 series power supply is designed to meet the following EMC immunity specifications:

Table 6. Environmental Specifications:

Document	Description
FCC Part 15 Subpart J / EN55022, Class A	Conducted and Radiated EMI Limits
EN61000-3-2	Harmonics
EN61000-3-3	Voltage Fluctuations
IEC/EN 61000-4-2	Electromagnetic Compatibility (EMC) - Testing and measurement techniques – Electrostatic discharge immunity test. +/-8KV air, +/-4KV contact discharge, performance Criteria B
IEC/EN 61000-4-3	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Radiated, radio-frequency, electromagnetic field immunity test, Criteria A
IEC/EN 61000-4-4	Electromagnetic Compatibility (EMC) - Testing and measurement techniques, Electrical Fast Transient/Burst Immunity Test. 2KV for AC power port, 1.0KV for DC ports, I/O and signal ports performance Criteria B
IEC/EN 61000-4-5	Electromagnetic Compatibility (EMC) - Testing and measurement techniques – 2KV common mode and 1KV differential mode for AC ports and 0.5kV differential mode for DC power, I/O and signal ports, performance criteria B.
IEC/EN 61000-4-11	Electromagnetic Compatibility (EMC) - Testing and measurement techniques : Voltage Dips and Interruptions: >30% reduction for 500ms, Criteria C,>95% reduction for 10mS, Criteria B, >95% reduction for 500mS, Criteria C
EN55022	Information Technology Equipment-Immunity Characteristics, Limits and Method of Measurements

Safety Certifications

The DS1100PED-3 power supply is intended for inclusion in other equipment and the installer must ensure that it is in compliance with all the requirements of the end application. This product is only for inclusion by professional installers within other equipment and must not be operated as a stand alone product.

Table 7. Safety Certifications for DS1100PED-3 series power supply system .

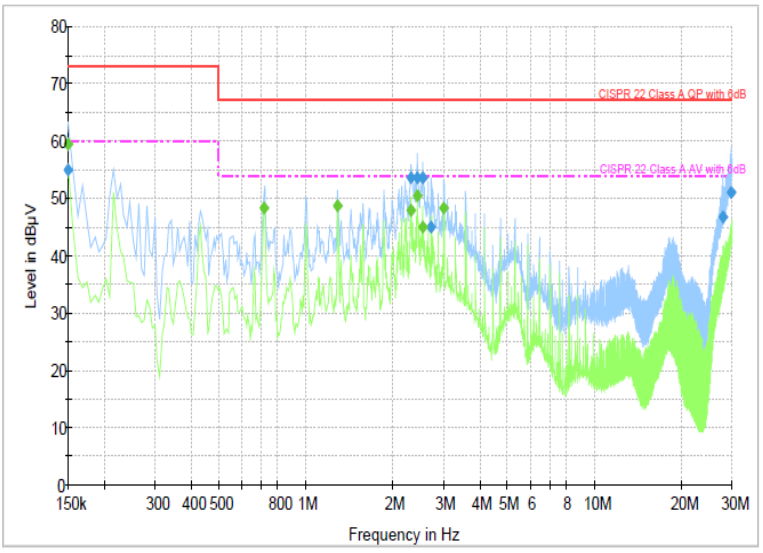
Document	File #	Description
UL 60950 No.	E186249-A239-UL	US and Canada Requirements
CE (LVD+RoHS)	13469	Europe Requirements
CB Certificate and Report	DK-32045-A1-UL	All CENELEC Countries
CHINA CCC Approval	2013010907623739	China Requirements (Exemption from 5000m altitude and tropical climate conditions)
DEMKO CERT	D-02416-A2	Europe Requirements
BSMI	CI332161602210 01	Bureau of Standards, Metrology and Inspection

EMI Emissions

The DS1100PED-3 series has been designed to comply with the Class A limits of EMI requirements of FCC Part 15 and CISPR 22 (EN55022) for emissions and relevant sections of EN61000 (IEC 61000) for immunity. The unit is enclosed inside a metal box, tested at 1100W using resistive load with cooling fan.

Conducted Emissions

The applicable standard for conducted emissions is EN55022 (FCC Part 15). Conducted noise can appear as both differential mode and common mode noise currents. Differential mode noise is measured between the two input lines, with the major components occurring at the supply fundamental switching frequency and its harmonics. Common mode noise, a contributor to both radiated emissions and input conducted emissions, is measured between the input lines and system ground and can be broadband in nature.



The DS1100PED-3 power supplies have internal EMI filters to ensure the convertors' conducted EMI levels comply with EN55022 (FCC Part 15) Class A and EN55022 (CISPR 22) Class A limits. The EMI measurements are performed with resistive loads at maximum rated loading.

Sample of EN55022 Conducted EMI Measurement at 230Vac input

Note: Red Line refers to Artesyn Quasi Peak margin, which is 6dB below the CISPR international limit. Pink Line refers to the Artesyn Average margin, which is 6dB below the CISPR international limit.

Conducted Emissions

Table 8. Conducted EMI emission specifications of the DS1100PED-3 series

Parameter	Model	Symbol	Min	Typ	Max	Unit
FCC Part 15 Subpart J, class A	All	Margin	-	-	6	dB
CISPR 22 (EN55022) class A	All	Margin	-	-	6	dB

Radiated Emissions

Unlike conducted EMI, radiated EMI performance in a system environment may differ drastically from that in a stand-alone power supply. The shielding effect provided by the system enclosure may bring the EMI level from Class A to Class B. It is thus recommended that radiated EMI be evaluated in a system environment. The applicable standard is EN55022 Class A (FCC Part 15). Testing ac-dc convertors as a stand-alone component to the exact requirements of EN55022 can be difficult, because the standard calls for 1m leads to be attached to the input and outputs and aligned such as to maximize the disturbance. In such a set-up, it is possible to form a perfect dipole antenna that very few ac-dc convertors could pass. However, the standard also states that 'an attempt should be made to maximize the disturbance consistent with the typical application by varying the configuration of the test sample.

Operating Temperature

The DS1100PED-3 power supplies will start and operate within stated specifications at an ambient temperature from 0 ^{O}C to 50 ^{O}C under all load conditions with internal fan. And the DS1100PED-3-001 power supplies will start and operate within stated specifications at an ambient temperature from 0 ^{O}C to 40 ^{O}C under all load conditions with internal fan. All the DS1100PED-3 series power supplies can withstand operation up to 60 ^{O}C at derated power without damage.

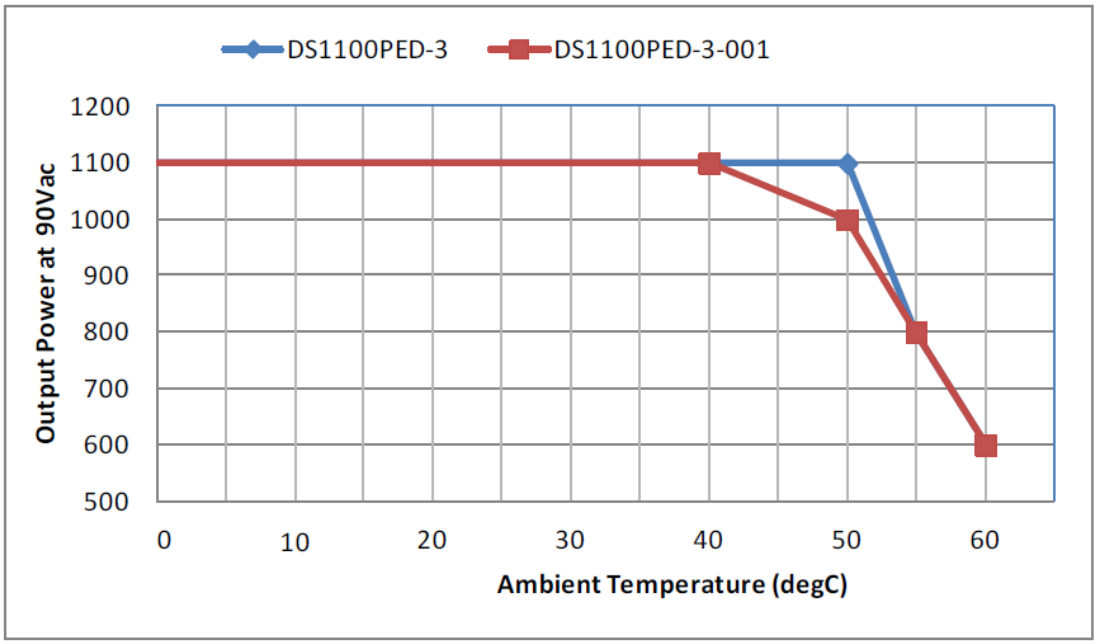
Forced Air Cooling

The DS1100PED-3 series power supplies included internal cooling fans as part of the power supply assembly to provide forced air-cooling to maintain and control temperature of devices and ambient temperature in the power supply to appropriate levels. The standard direction of airflow is from the DC connector end to the AC connector end of the power supply.

The cooling fan is a variable speed fan. In Standby mode power supply fan will operate at minimum speed to maintain component reliability at all load, line and ambient conditions. When 12V output is enabled, power supply fan will operate at minimum achievable fan speed. Power supply fan speed control algorithms will vary the speed so that the critical component temperatures do not exceed safe operating levels. Fans will be powered from voltage source inside the power supply and from system side voltage source.

Power Derating Curves

DS1100PED-3 total output power will be derated according to the curve shown below



Storage and Shipping Temperature / Humidity

The DS1100PED-3 series power supplies can be stored or shipped at temperatures between –40 ºC to +70 ºC and relative humidity from 10% to 95% non-condensing.

Altitude

The DS1100PED-3 series will operate within specifications at altitudes up to 10,000 feet above sea level. The power supply will not be damaged when stored at altitudes of up to 50,000 feet above sea level.

Humidity

Operating: Power supply is designed to operate with no degradation of performance while operating in range of 20%RH to 80%RH non-condensing.

Non-Operating: Power supply is designed to operate with no degradation of performance while operating in range of 10%RH to 95%RH non-condensing.

Vibration

The DS1100PED-3 power supply will pass the following vibration specifications:

Non-Operating Random Vibration

Acceleration	2.5	gRMS
Frequency Range	10-200; 200-2000	Hz
Duration	20	Mins per axis
Direction	Rotating each axis on vertical vibration	
PSD Profile	**FREQ** 10-200Hz; 200-2000Hz — **SLOPE dB/oct** — **PSD g^2/Hz** 0.01 0.003	

Operating Random Vibration

Acceleration	1.0	gRMS
Frequency Range	10-500	Hz
Duration	20	Mins per axis
Direction	Rotating each axis on vertical vibration	
PSD Profile	**FREQ** 10-500 Hz — **SLOPE dB/oct** — **PSD g^2/Hz** 0.002	

Shock

The DS1100PED-3 power supply will pass the following vibration specifications:

Non-Operating Half-Sine Shock

Acceleration	30	G
Duration	6	msec
Pulse	Half-Sine	
No. of Shock	3 times in each of 6 faces (each positive and negative directions)	

Operating Half-Sine Shock

Acceleration	4	G
Duration	22	msec
Pulse	Half-Sine	
No. of Shock	3 times in each of 6 faces (each positive and negative directions)	

Power and Control Signal Descriptions

AC Input Connector

This connector supplies the AC Mains to the DS1100PED-3 power supply.

Pin 1 - L1
Pin 2 - L2
Pin 3 - Earth Ground

Output Connector – Power Blades

These pins provide the main output for the DS1100PED-3. The + Main Output (V_O) and the Main Output Return pins are the positive and negative rails, respectively, of the V_O main output of the DS1100PED-3 power supply. The Main Output (V_O) is electrically isolated from the power supply chassis.

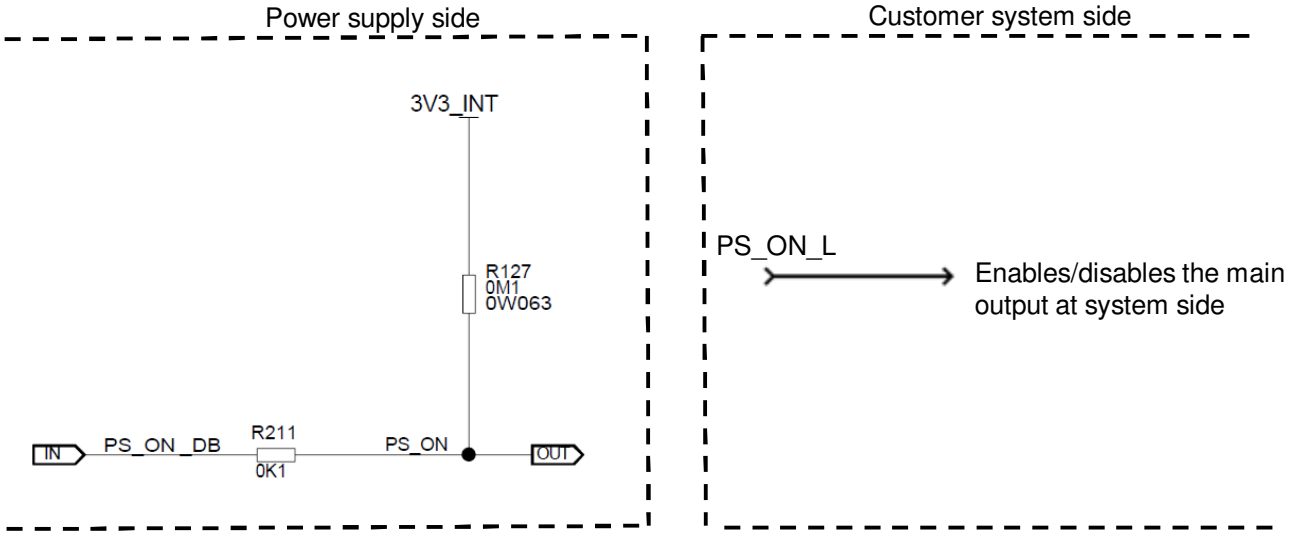
P1-P8 - + Main Output (V_O)
P9-P18 - Main Output Return
P19-P20 - Standby Output (Vsb)
P21-P28 - Main Output / Standby Return
P29-P36 - + Main Output (V_O)

Output Connector - Control Signals

The DS1100PED-3 series contains a 24 pins control signal header providing an analogue control interface, Standby power and I2C interface signal connections.

PS_ON_L – (pin S13)

This signal input pin controls the normal turning ON and Off of the Main Output of the DS1100PED-3 power supply. The power supply main output (V_O) will be enabled when this signal is pulled low, below 0.8 V. The Power supply output (except Vsb output) will be disabled when this input is driven higher than 2.0 V, or left open circuited.



Main Output Remote Sense Return, Main Output Remote Sense – (pins S21, S23)

The main output of the DS1100PED-3 is equipped with a Remote Sensing capability that will compensate for a power path drop around the entire loop of 200 millivolt. This feature is implemented by connecting the Main Output Remote Sense (pin S23) and the Main Output Remote Sense Return (pin S21) to the positive and negative rails of the main output, respectively, at a location that is near to the load. Care should be taken in the routing of the sense lines as any noise sources or additional filtering components introduced into the voltage rail may affect the stability of the power supply. The DS1100PED-3 will operate appropriately without the sense lines connected; however it is recommended that the sense lines be connected directly to the main output terminals if remote sensing is not required. This remote sense circuit will not raise the power supply's output voltage to the OVP trip level. Main Output Remote Sense has no effect on the Standby Output (Vsb).

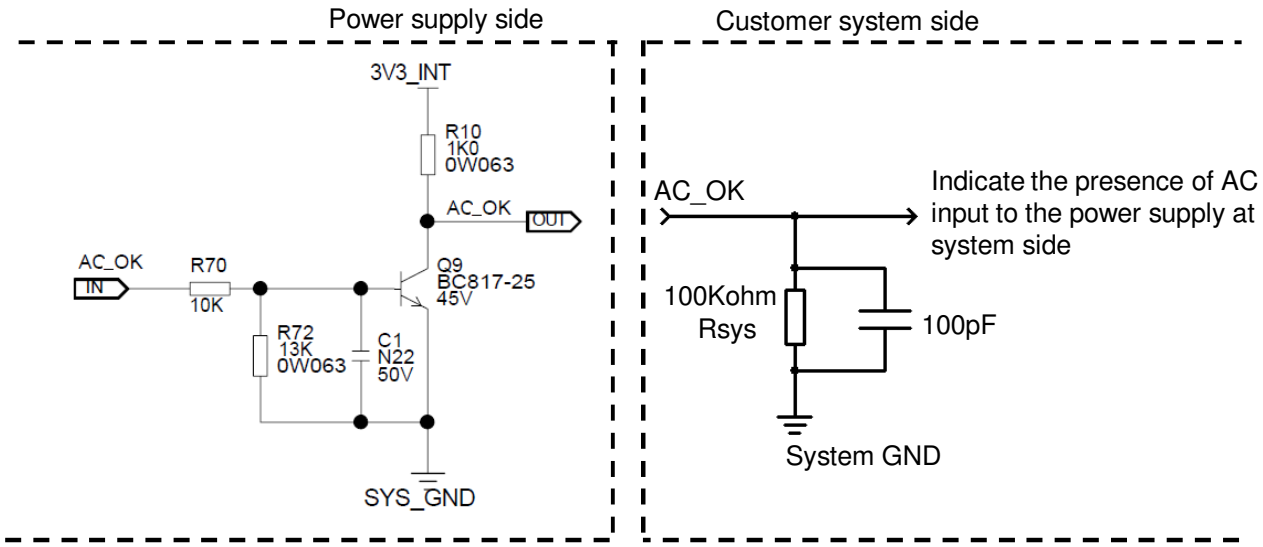
12V Main output and Standby output return lines are connected together inside PSU and connected to PSU chassis directly. It is recommended to connect 12V return to system chassis on end system application for better common mode noise.

Standby Output, Standby Output Return – (pins P19-P20, P21-P28)

The DS1100PED-3 provides a regulated 12 volt 3 amp auxiliary output voltage to power critical circuitry that must remain active regardless of the on/off status of the power supply's main output. The Standby Output (Vsb) voltage is available whenever a valid AC input voltage is applied to the unit. The Standby Output is independently short circuit protected and is referenced to the Standby Output Return pins.

AC_OK – (pin S5)

Signal used to indicate the presence of AC input to the power supply. A logic level HIGH will indicate that the AC input to the power supply is within the operating range while a logic level LOW will indicate that AC has been lost. This is an common collector/drain output. This pin is pulled high by a 1.0kohm resistor connected to 3.3V inside the power supply. It is recommended that this pin be connected to a 100pF decoupling capacitor and pulled down by a 100kohm resistor.



ISHARE – (pin S7)

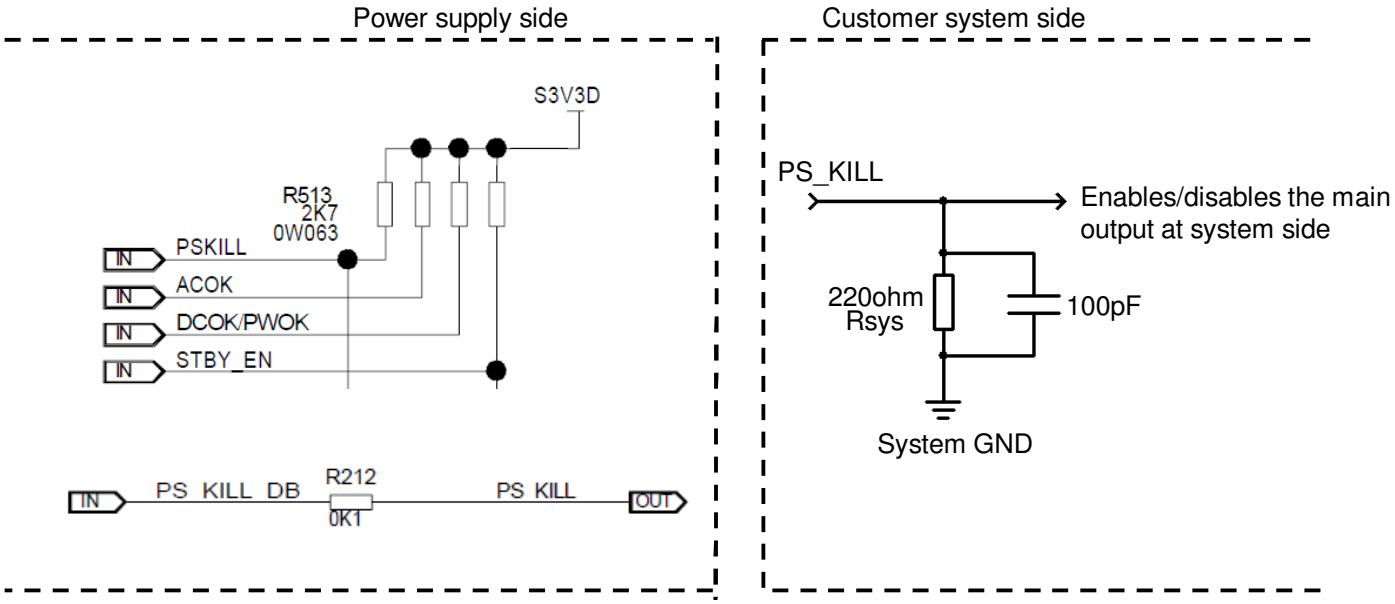
The DS1100PED-3 supports active current sharing through a single wire connection between the power supplies. This input/output signal pin allows two or more power supplies to share the main output load current to increase the overall power capability or to operate the units in a N+N configuration for redundancy purposes.

The voltage of this signal will be a linear slope from no load to full load. At 45.8A of each power supply output when two supplies are running in parallel, the ISHARE voltage will be between 3.85V and 4.15V. At 91.6A of each power supply output when two supplies are running in parallel, the ISHARE voltage will be between 7.75V and 8.25V.

If any power supply is hot swapped, no glitch will occur that violates the regulation limits of the power supply defined in this specification.

PS_KILL_L – (pin S14)

First break/Last Mate active LOW signal which enables/disables the main output. This signal will have to be pulled to ground at the system side with a 220ohm resistor. A 100pF decoupling capacitor is also recommended (Standby output will remain on).



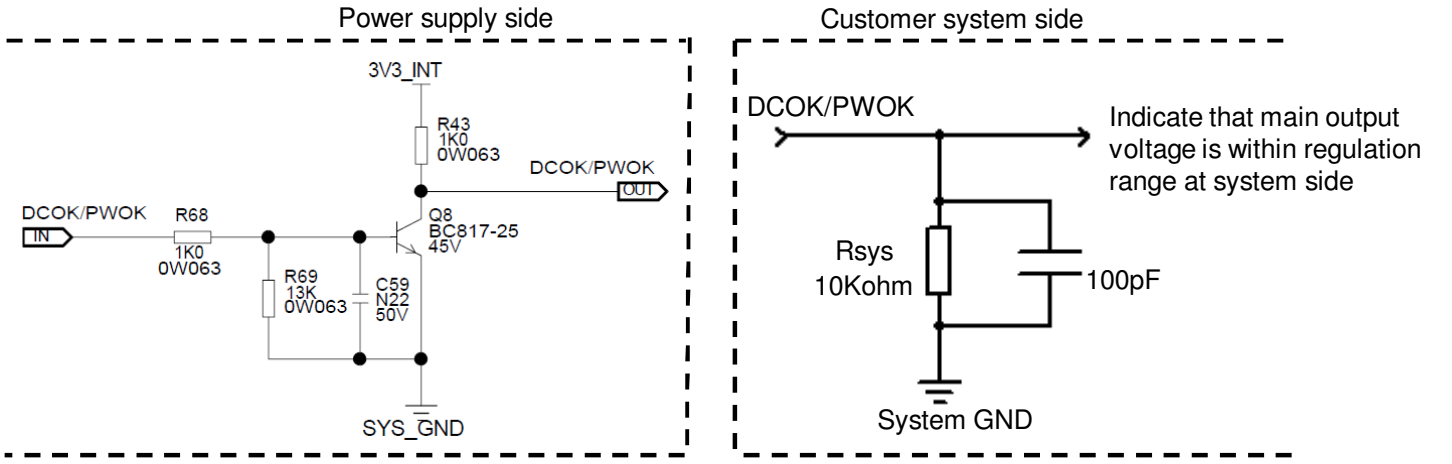
SDA, SCL and S_INTERRUPT_L – (pins S17, S19, S9)

Please refer to “Communication Bus Descriptions” section.

Pwr_Good(DCOK/PWOK) – (pin S4)

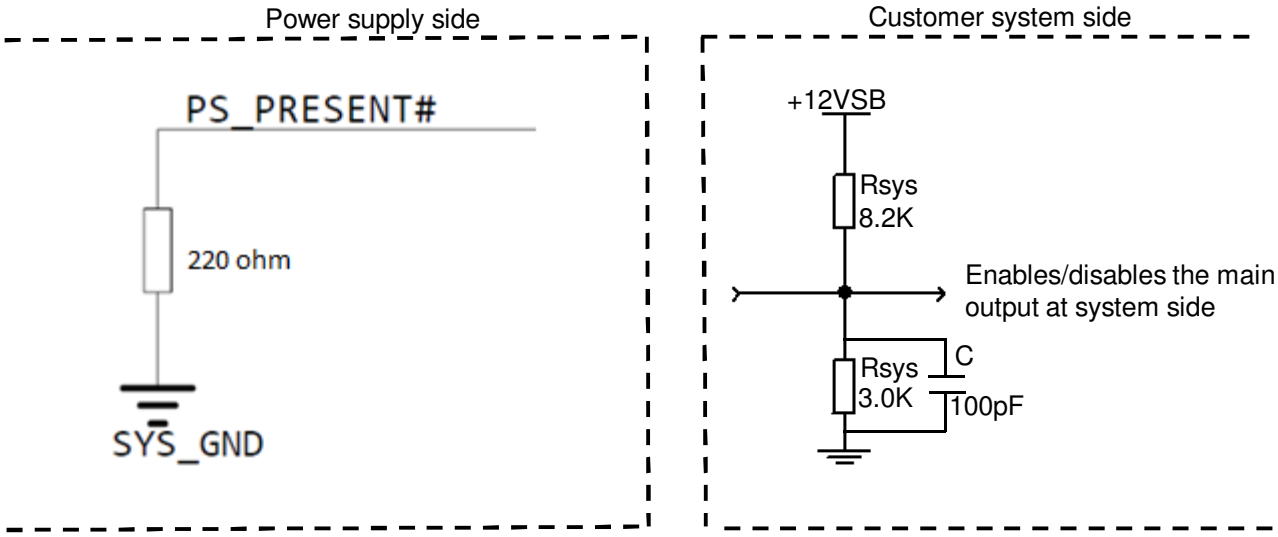
Signal used to indicate that main output voltage is within regulation range. The Pwr_Good signal will be driven HIGH when the output voltage is valid and will be driven LOW when the output falls below the under-voltage threshold. This signal also gives an advance warning when there is an impending power loss due to loss of AC input or system shutdown request.

This is an common collector/drain output. This pin is pulled high by a 1.0kohm resistor connected to 3.3V inside the power supply. It is recommended that this pin be connected to a 100pF decoupling capacitor and pulled down by a 10kohm resistor.



PS_PRESENT – (pin S1)

Signal used to indicate to the system that a power supply is inserted in the power bay. This pin is internally pulled down to the standby return in the power supply with a 220 ohms resistor. If using a Multi-meter measure resistance between PS_PRESENT pin and +12VSB_RETURN of output connector. Measured resistance should be between 215 ohms to 225 ohms. Recommended pull-up resistor to 12Vsb is 8.2k ohm with a 3.0k ohm pull-down to ground. A 100pF decoupling capacitor is also recommended.



Communication Bus Descriptions

I2C Bus Signals

The DS1100PED-3 power supply contains enhanced monitor and control functions implemented via the I2C bus. The DS1100PED-3 I2C functionality (PMBus™ and FRU data) can be accessed via the output connector control signals. The communication bus is powered either by the internal 3.3V supply or from an external power source connected to the Standby Output (ie: accessing an unpowered power supply as long as the Standby Output of another power supply connected in parallel is on).

If units are connected in parallel or in redundant mode, the Standby Outputs must be connected together in the system. Otherwise, the I2C bus will not work properly when a unit is inserted into the system without the AC source connected.

Note - PMBus™ functionality can be accessed only when the PSU is powered-up.
Guaranteed communication I2C speed is 100KHz.

SDA, SCL (I2C Data and Clock Signals) – (pins S17, S19)

I2C serial data and clock bus - these pins are internally pulled up to internal 3.3V supply with a 100K resistor. These pins must be pulled-up in the system by an 2.2K ohm resistor to 3.3V and a 100pF decoupling capacitor at the system side.

PS_INTERRUPT_L – (pin S9)

PS_INTERRUPT_L is used to send a signal to the system that a fault in the power supply occurred. This signal is normally logic level HIGH. It will go to a LOW logic level when a fault bit has been set in the power supply's status register. This event can be triggered by faults such as OVP, OCP, OTP, and fan fault. This signal can be cleared by a CLEAR_FAULT command. Recommended pull-up resistor to 12Vsb is 8.2k with a 3.0k pull-down to ground. A 100pF decoupling capacitor is also recommended.

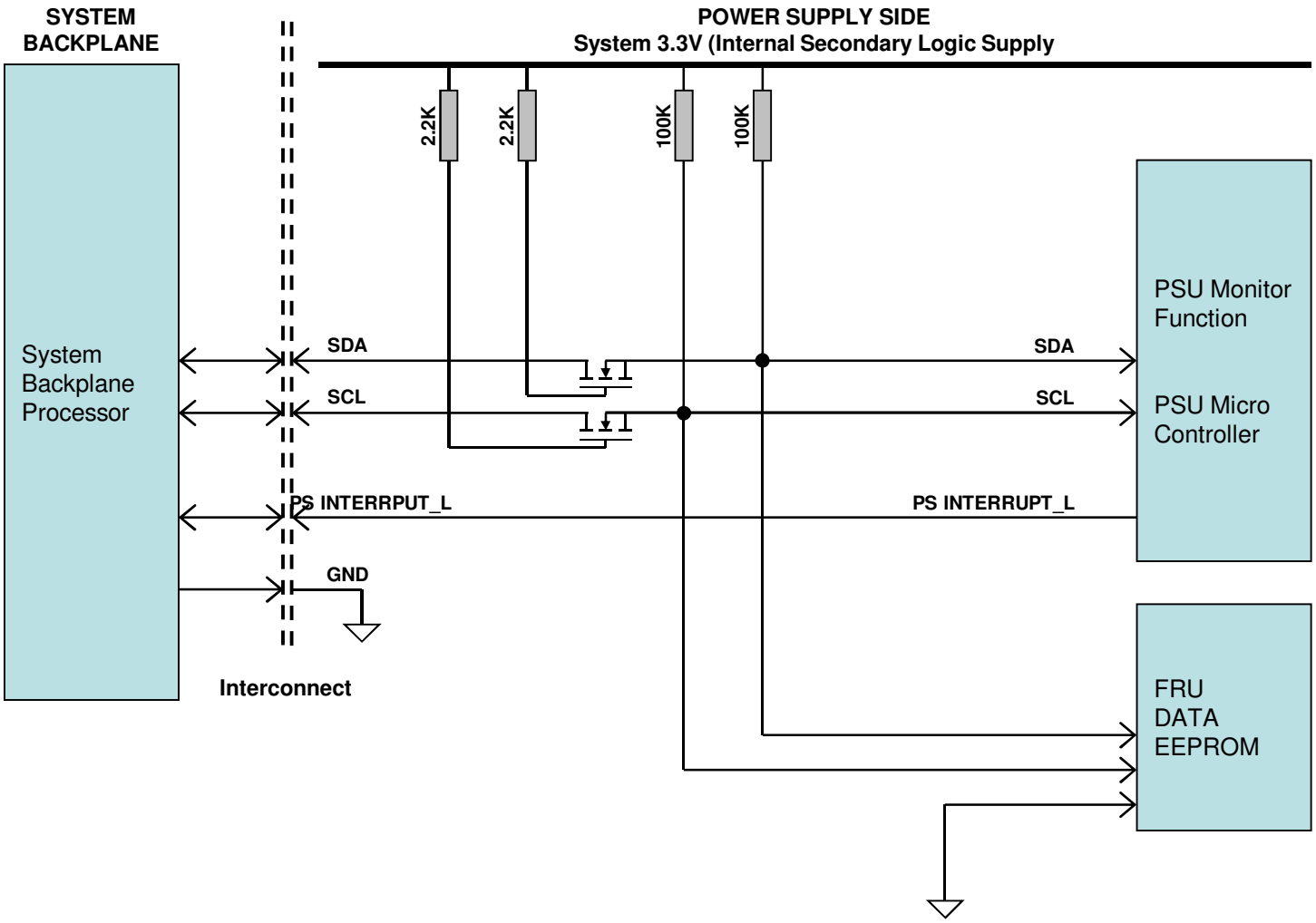
I2C Bus Communication Interval

The interval between two consecutive I2C communications to the power supply should be at least 15 ms to ensure proper monitoring functionality.

I2C Bus Signal Integrity

The noise on the I2C bus (SDA, SCL lines) due to the power supply will be less than 300mV peak-to-peak. This noise measurement should be made with an oscilloscope bandwidth limited to 100MHz. Measurements should be make at the power supply output connector with 2.2K ohm resistors pulled up to Standby Output and 100pf ceramic capacitors to Standby Output Return.

I2C Bus Internal Implementation, Pull-ups and Bus Capacitances



I2C Bus - Recommended external pull-ups:

Electrical and Interface specifications of I2C signals (referenced to Standby Output Return pin, unless otherwise indicated):

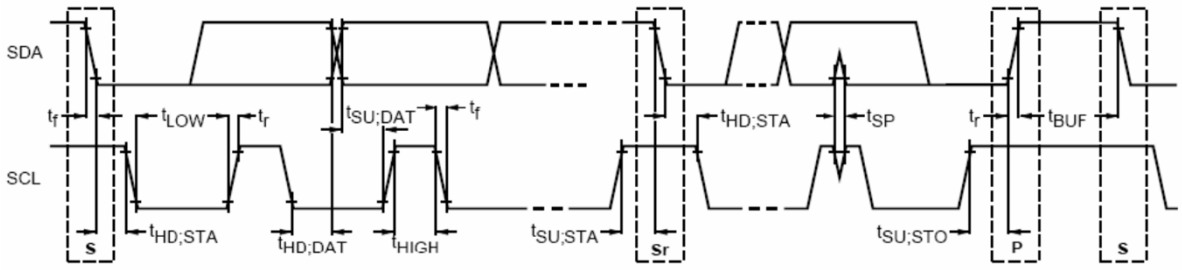
Parameter	Condition	Symbol	Min	Typ	Max	Unit
SDA, SCL internal pull-up resistor		R_{int}	-	100	-	Kohm
SDA, SCL internal bus capacitance		C_{int}	-	0	-	pF
SDA, SCL external maximum allowed capacitance on system board		C_{ext}	-	-	200	pF
Recommended external pull-up resistor	1 PSU	R_{ext}	-	2.2	-	Kohm
	6 PSU		-	0.37	-	Kohm

Logic Levels

DS1100PED-3 series power supply I2C Communication Bus will respond to logic levels as per below:

Logic High: 3.3V Nominal (Specs is 2.0V to 5.0V)**
Logic Low: 500mV nominal (Specs is 800mV max)**
** Note - Artesyn 73-769-001 I2C adapter was used.

Timings



Parameter	Symbol	Standard-Mode Soecs		Actual		Unit
		Min	Max			
SCL Clock Frequency	f_{SCL}	0	100	100		KHz
Hold time (repeated) START condition	$t_{HD;STA}$	4.0	-	5.0		us
LOW period of SCL clock	t_{LOW}	4.7	-	14.9		us
HIGH period of SCL clock	t_{HIGH}	4.0	-	5.0		us
Setup time for repeated START condition	$t_{SU;STA}$	4.7	-	15.5		us
Data hold time	$t_{HD;DAT}$	0	3.45	1.56		us
Data setup time	$t_{SU;DAT}$	250	-	2400		ns
Rise time	t_r	-	1000	SCL = 800	SDA = 800	ns
Fall time	t_f	-	300	SCL = 100	SDA = 100	ns
Setup time for STOP condition	$t_{SU;STO}$	4.0	-	4.9		us
Bus free time between a STOP and START condition	t_{BUF}	4.7	-	30.3		msec

*** Note - Artesyn 73-769-001 I2C adapter (USB-to-I2C) and Universal PMBus GUI software was used.

Device Addressing

The DS1100PED-3 has a fixed I2C address 0xB0. This address has been set in the power supply side, there is no address bit accessible externally. In order to support multiple addresses, system side should use an I2C switcher or I2C expander. Contact Artesyn for the demo and application note of I2C switcher or I2C expander.

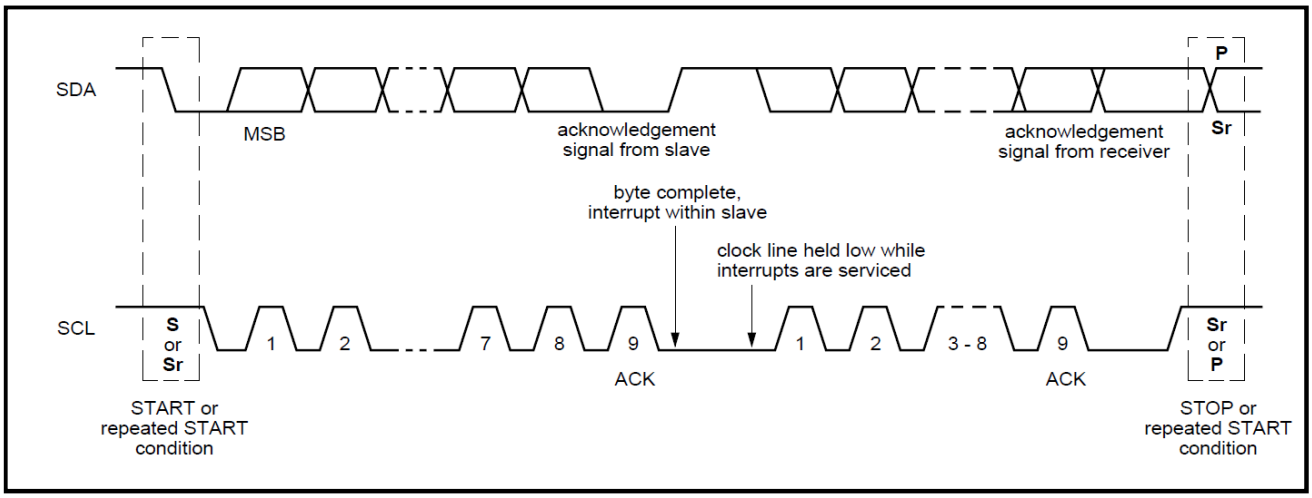
Contact Artesyn for availability of a variant model supporting multiple addresses.

Pull signaling pins S2, S3, and S24 at the system side to low for I2C addressing compatibility across all models in the short family of Front-end Bulk Power Series including the DS500SPE, DS750PED, DS1100PED and DS1600SPE.

I2C Clock Synchronization

The DS1100PED-3 power supply might apply clock stretching. An addressed slave power supply may hold the clock line (SCL) low after receiving (or sending) a byte, indicating that it is not yet ready to process more data. The system master that is communicating with the power supply will attempt to raise the clock to transfer the next bit, but must verify that the clock line was actually raised. If the power supply is clock stretching, the clock line will still be low (because the connections are open-drain).

The maximum transaction timeout condition for clock stretching for DS1100PED-3 is 100 milliseconds. The maximum clock low timeout condition for clock stretching is 25 milliseconds.



FRU (EEPROM) Data

The FRU (Field Replaceable Unit) data format is compliant with the Intel IPMI v1.0 specification.

The DS1100PED-3 uses 1 page of EEPROM for FRU purpose. A page of EEPROM contains up to 255 byte-sized data locations.

Where: OFFSET - The OFFSET denotes the address in decimal format of a particular data byte within DS1100PED-3 EEPROM.

VALUE - The VALUE details data written to a particular memory location of the EEPROM.

DEFINITION - The contents DEFINITION refers to the definition of a particular data byte.

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		COMMON HEADER, 8 BYTES		
0	00	**FORMAT VERSION NUMBER** (Common Header) 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
1	01	**INTERNAL USE AREA OFFSET**	27	1B
2	02	**CHASSIS INFO AREA OFFSET**	1	01
3	03	**BOARD INFO AREA OFFSET**	0	00
4	04	**PRODUCT INFO AREA OFFSET**	5	05
5	05	**MULTI RECORD AREA OFFSET**	13	0D
6	06	**PAD** (reserved) Default value is 0.	0	00
7	07	**ZERO CHECK SUM** (256 – (Sum of bytes 0 to 6))	209	D1
		CHASSIS INFO AREA(32 BYTES) This area will be filled by the Mfg. Diag. or by the OS if used		
8	08	**FORMAT VERSION NUMBER** 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
9	09	**CHASSIS INFO AREA LENGTH** in multiple of 8 bytes	4	04
10	0A	**CHASSIS TYPE** (Default value is 0.)	0	00
11	0B	**CHASSIS PART NUMBER** Type/Length CAh (if used) Type = “ASCII+LATIN1” = (11)b Length = 10 Bytes = (001010)b	202	CA
12	0C	**CHASSIS PART NUMBER BYTES** (Default value is 0.)	0	00
13	0D		0	00
14	0E		0	00
15	0F		0	00
16	10		0	00
17	11		0	00
18	12		0	00
19	13		0	00
20	14		0	00
21	15		0	00
22	16	**CHASSIS SERIAL NUMBER Type/Length** CFH (if used) Type = “ASCII+LATIN1” = (11)b Length = 15 Bytes = (001111)b	207	CF
23	17	**CHASSIS SERIAL NUMBER BYTES,** Default value is 0.	0	00
24	18		0	00
25	19		0	00
26	1A		0	00
27	1B		0	00
28	1C		0	00
29	1D		0	00
30	1E		0	00
31	1F		0	00
32	20		0	00

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
33	21	CHASSIS SERIAL NUMBER BYTES, Default value is 0.	0	00
34	22		0	00
35	23		0	00
36	24		0	00
37	25		0	00
38	26	**End Tag** (0C1h if used)	193	C1
39	27	**CHKSUM** (Zero CHKSUM if used)	161	A1
		PRODUCT INFORMATION AREA, 64 BYTES		
40	28	**FORMAT VERSION NUMBER** (Product Info Area) 7:4 - Reserved, write as 0000b 3:0 - Format Version Number = 1h for this specification	1	01
41	29	**PRODUCT INFO AREA LENGTH** (In multiples of 8 bytes)	8	08
42	2A	**Language (English)**	25	19
43	2B	**MANUFACTURER NAME TYPE / LENGTH** (0C5H) 7:6 - (11)b, 8-Bit ASCII+Latin 1, 5:0 – (000101)b, 5-Byte Allocation	199	C7
		MANUFACTURER'S NAME 7 byte sequence		
44	2C	“A”= 45h	69	45
45	2D	“R”= 4Dh	77	4D
46	2E	“T”= 45h	69	45
47	2F	“E”= 52h	82	52
48	30	“S”= 53h	83	53
49	31	“Y”= 4Fh	79	4F
50	32	“N”= 4Eh	78	4E
51	33	**PRODUCT NAME** Type/Length (CFH) Type = “ASCII+LATIN1” = (11)b Length = 15 Bytes = (001111)b	207	CF
		PRODUCT NAME BYTES (15 Byte sequence)		
52	34	“D”	68	44
53	35	“S”	83	53
54	36	“1”	49	31
55	37	“1”	49	31
56	38	“0”	48	30
57	39	“0”	48	30
58	3A	“P”	80	50
59	3B	“E”	69	45
60	3C	“D”	68	44
61	3D	“-”	45	2D
62	3E	“3”	51	33
63	3F		32	20
64	40		32	20
65	41		32	20
66	42		32	20
67	43	**PRODUCT PART/MODEL NUMBER** Type/Length (CFH) Type = “ASCII+LATIN1” = (11)b Length = 15 Bytes = (001111)b	207	CF
		PRODUCT NAME BYTES (15 Byte sequence)		
68	44	“D”	68	44
69	45	“S”	83	53
70	46	“1”	49	31
71	47	“1”	49	31
72	48	“0”	48	30
73	49	“0”	48	30
74	4A	“P”	80	50
75	4B	“E”	69	45
76	4C	“D”	68	44
77	4D	“-”	45	2D

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
78	4E	"3"	51	33
79	4F		32	20
80	50		32	20
81	51		32	20
82	52		32	20
83	53	**PRODUCT VERSION NUMBER** Type/Length (C2h) Type = "ASCII+LATIN1" = (11)b Length = 2 bytes = (000010)b	194	C2
		PRODUCT VERSION NUMBER BYTES Refer to Section 1.2 Product Revision History in latest IPS		
84	54	"A"	65	41
85	55	"A"	65	41
		PRODUCT SERIAL NUMBER Type/Length		
86	56	Type = "ASCII+LATIN1" = (11)b Length = 13 bytes = (001101)b	205	CD
		PRODUCT SERIAL NUMBER BYTES Model ID = DS1100PED-3 / K369		
87	57	"K"	75	4B
88	58	"3"	51	33
89	59	"6"	54	36
90	5A	"9"	57	39
		MANUFACTURING YEAR AND WEEK CODE		
91	5B	"W"=57h (Per Unit)	87	57
92	5C	"W"=57h (Per Unit)	87	57
		UNIQUE SERIAL NUMBER "SSSS"		
93	5D	"S" = 53 (Per Unit)	83	53
94	5E	"S" = 53 (Per Unit)	83	53
95	5F	"S" = 53 (Per Unit)	83	53
96	60	"S" = 53 (Per Unit)	83	53
		MODEL REVISION, Astec Model Rev, See Latest Model Rev in IPS Sec 1.2		
97	61	"A"	65	41
98	62	"A"	65	41
		MANUFACTURING LOCATION "P" for "Laguna, Philippines" In Decimal = 080 In Hex = 50H		
99	63	"C" for "Cavite, Philippines" In Decimal = 067 In Hex = 43H "F" for "Fuyong, China" In Decimal = 070 In Hex = 46H	70	46
100	64	End Tag	193	C1
101	65	**PAD (reserved), Default value is 0.**	0	00
102	66		0	00
		ZERO CHECK SUM (256 – (Sum of bytes 40 to 102)) Per Unit		
103	67	Zero Check Sum :Should follow check sum calculation as per IPMI v1.1 specs	187	BB
		Multi Record Area, 88 Bytes		
		Power Supply Record Header		
104	68	Record type = 00 for Power supply	0	00
105	69	End of List /Record Format Version Number	2	02
106	6A	Record Length of Power Supply Record	24	18
107	6B	Record CHECKSUM of Power Supply Record (Zero CHECKSUM) (256-(sum of bytes 109 to 132)	23	17/CC
108	6C	Header CHECKSUM of Power Supply Record Header (Zero CHECKSUM) (256-(sum of bytes 104 to 107)	207	CF/1A
		Power Supply Record		
		Overall Capacity of the Power Supply 2 Bytes Sequence 1100W = 044CH		
109	6D	In Decimal = 76, 04	76	4C
110	6E	In Hex = 4CH, 04H	4	04

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Peak VA, 1260W = 04ECH		
		2 Bytes Sequence		
111	6F	In Decimal = 236, 004	236	EC
112	70	In Hex = ECH, 04H	4	04
		Inrush Current, 55A		
113	71	In Decimal = 055	55	37
		In Hex = 37H		
		Inrush Interval, 10mS		
114	72	In Decimal = 010	10	0A
		In Hex = 0AH		
		Low End Input Voltage Range 1(10mV), (90V / 10mV) 9000 = 2328H		
		2 Bytes Sequence		
115	73	In Decimal = 040, 035	40/80	28/50
116	74	In Hex = 28H, 23H	35/70	23/46
		High End Input Voltage Range 1(10mV), (264V/10mV) 26400= 6720H		
		2 Bytes Sequence		
117	75	In Decimal = 032, 103	32	20
118	76	In Hex = 20H, 67H	103	67
		Low End Input Voltage Range 2(10mV)		
119	77	Not Applicable	0	00
120	78	(Autoswitch)	0	00
		High End Input Voltage Range 2(10mV)		
121	79	Not Applicable	0	00
122	7A	(Autoswitch)	0	00
123	7B	**Low End Input Frequency Range,** 47Hz = 2FH	47	2F
124	7C	**Low End Input Frequency Range,** 63Hz = 3FH	63	3F
125	7D	**AC Dropout Tolerance in ms,** 10mS= 0AH	10	0A
126	7E	**Binary Flags,** 1 indicates function supported and a 0 indicates function not supported. Bits 7-5: RESERVED, WRITE AS 000B Bit 5: PMBUS capable or not. 1 if Supported 0 if not. BIT = 1 Bit 4: Tachometer Pulses Per Rotation / Predictive Fail Polarity BIT = 0 Bit 3: Hot Swap / Redundancy Support BIT = 1 Bit 2: Auto switch Support BIT = 1 Bit 1: Power Factor Correction Support BIT = 1 Bit 0: Predictive Fail Support BIT = 0	46	2E
		Peak Wattage Capacity and Holdup Time		
		2 Bytes Sequence		
127	7F	1100W = 044CH	76	4C
128	80	10ms = 0AH	164	A4
		Combined Wattage, Not Applicable, 3 Bytes Sequence Byte 1: 0000 0000 0000 0000 Byte 2 and Byte 3: 00H, 00H		
129	81		0	00
130	82		0	00
131	83		0	00
		Predictive Fail Tachometer Lower Threshold, Not Applicable.		
132	84	Predictive Failure is not Supported.	0	00
12V DC OUTPUT RECORD HEADER				
133	85	Record type = 01 for DC Output Record	1	01
134	86	End of List /Record Format Version Number for 12V DC Output Record	2	02
135	87	Record Length of 12V DC Output Record	13	0D
136	88	Record CHECKSUM of 12V DC Output Record (Zero CHECKSUM) (256-(sum of bytes 138 to 150)	184	B8
137	89	Header CHECKSUM of 12V DC Output Record Header (Zero CHECKSUM) (256-(sum of bytes 1313 to 136)	56	38

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		12V OUTPUT RECORD		
138	8A	**Output Information, 001 = 01H** Bit 7: Standby Information = 0B Bits 6-4: Reserved, Write as 000B Bits 3-0: Output Number 1 = 001B	1	01
		Nominal Voltage (10mV), (12V / 10mV) 1200 = 04B0H 2 Bytes Sequence		
139 140	8B 8C	In Decimal: 176, 004 In Hex: B0H, 04H	176 4	B0 04
		Maximum Negative Voltage Deviation (10mV), 1140 = 0474H 2 Bytes Sequence		
141 142	8D 8E	In Decimal: 116, 004 In Hex: 74H, 04H	116 4	74 04
		Maximum Positive Voltage Deviation (10mV), 1260 =04ECH 2 Bytes Sequence		
143 144	8F 90	In Decimal: 236, 004 In Hex: ECH, 04H	236 4	EC 04
		Ripple and Noise pk-pk (mV), 120 = 78H 2 Bytes Sequence		
145 146	91 92	In Decimal: 120, 000 In Hex: 78H, 00H	120 0	78 00
		Minimum Current Draw (10mA), 0200 = 00C8H 2 Bytes Sequence		
147 148	93 94	In Decimal: 200, 000 In Hex: C8H, 00H	200 0	C8 00
		Maximum Current Draw (10mA), 9160 = 23C8H		
149 150	95 96	In Decimal: 200, 035 In Hex: C8H, 23H	200 35	C8 23
		Vsb OUTPUT RECORD HEADER		
151	97	Record type = 01 for DC Output Record	1	01
152	98	End of List /Record Format Version Number for 3V3SB Output Record	2	02
153	99	Record Length of 3V3SB Output Record	13	0D
154	9A	Record CHECKSUM of 3V3SB Output Record (Zero CHECKSUM) (256-(sum of bytes 156 to 168)	179	B3
155	9B	Header CHECKSUM of 3V3SB Output Record Header (Zero CHECKSUM) (256-(sum of bytes 151 to 154)	61	3D
156	9C	**Output Information,** 002 = 02H Bit 7: Standby Information = 1B Bits 6-4: Reserved, Write as 000B Bits 3-0: Output Number 2 = 010B	130	82
		Nominal Voltage (10mV), (12V / 10mV) 1200 = 04B0H 2 Bytes Sequence		
157 158	9D 9E	In Decimal: 176, 004 In Hex: B0H, 04H	176 4	B0 04
		Maximum Negative Voltage Deviation (10mV), 1140 = 0474H 2 Bytes Sequence		
159 160	9F A0	In Decimal: 116, 004 In Hex: 74H, 04H	116 4	74 04
		Maximum Positive Voltage Deviation (10mV), 1260 =04ECH 2 Bytes Sequence		
161 162	A1 A2	In Decimal: 236, 004 In Hex: ECH, 04H	236 4	EC 04
		Ripple and Noise pk-pk (mV), 120 = 78H 2 Bytes Sequence		
163 164	A3 A4	In Decimal: 120, 000 In Hex: 78H, 00H	120 0	78 00

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		Minimum Current Draw (10mA), (0.1A / 10mA) 10 = 000AH		
		2 Bytes Sequence		
165	A5	In Decimal: 010, 000	10	0A
166	A6	In Hex: 0AH, 00H	0	00
		Maximum Current Draw (10mA), (3A / 10mA) 300 = 012CH		
		2 Bytes Sequence		
167	A7	In Decimal: 044, 001	44	2C
168	A8	In Hex: 44H, 01H	1	01
		OEM RECORD HEADER		
169	A9	Record type = C0H for OEM Record	192	C0
170	AA	End of List /Record Format Version Number for 3.3Vsb output Record	130	82
171	AB	Record Length of OEM Record	42	2A
172	AC	Record CHECKSUM of OEM Record (Zero CHECKSUM)	0	00
173	AD	Header CHECKSUM of OEM Record Header (Zero CHECKSUM) (256-(sum of bytes 169 to 172)	148	94
		OEM RECORD		
174	AE	**Manufacturer ID** (3 bytes, Default is 0)	0	00
175	AF	RESERVED	0	00
176	B0	RESERVED	0	00
177	B1	RESERVED	0	00
178	B2	RESERVED	0	00
179	B3	RESERVED	0	00
180	B4	RESERVED	0	00
181	B5	RESERVED	0	00
182	B6	RESERVED	0	00
183	B7	RESERVED	0	00
184	B8	RESERVED	0	00
185	B9	RESERVED	0	00
186	BA	RESERVED	0	00
187	BB	PAD (reserved), Default value is 0.	0	00
188	BC		0	00
189	BD		0	00
190	BE		0	00
191	BF		0	00
192	C0		0	00
193	C1		0	00
194	C2		0	00
195	C3		0	00
196	C4		0	00
197	C5		0	00
198	C6		0	00
199	C7		0	00
200	C8		0	00
201	C9		0	00
202	CA		0	00
203	CB		0	00
204	CC		0	00
205	CD		0	00
206	CE		0	00
207	CF		0	00
208	D0		0	00
209	D1		0	00
210	D2		0	00
211	D3		0	00
212	D4		0	00
213	D5		0	00
214	D6		0	00
215	D7		0	00

DS1100PED-3 FRU (EEPROM) Data:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		INTERNAL USE AREA, 40 BYTES		
216	D8	RESERVED, Default value is 0.	0	00
217	D9		0	00
218	DA		0	00
219	DB		0	00
220	DC		0	00
221	DD		0	00
222	DE		0	00
223	DF		0	00
224	E0		0	00
225	E1		0	00
226	E2		0	00
227	E3		0	00
228	E4		0	00
229	E5		0	00
230	E6		0	00
231	E7		0	00
232	E8		0	00
233	E9		0	00
234	EA		0	00
235	EB		0	00
236	EC		0	00
237	ED		0	00
238	EE		0	00
239	EF		0	00
240	F0		0	00
241	F1		0	00
242	F2		0	00
243	F3		0	00
244	F4		0	00
245	F5		0	00
246	F6		0	00
247	F7		0	00
248	F8		0	00
249	F9		0	00
250	FA		0	00
251	FB		0	00
252	FC		0	00
253	FD		0	00
254	FE		0	00
255	FF	Zero CHECKSUM of Internal Use Area (if used). Default Value=0	0	00

DS1100PED-3-001 FRU (EEPROM) deviations:

OFFSET		DEFINITION	SPEC VALUE	
(DEC)	(HEX)	(REMARKS)	(DEC)	(HEX)
		PRODUCT INFORMATION AREA, 56 BYTES		
		PRODUCT NAME BYTES (15 Byte sequence)		
52	34	“D”	68	44
53	35	“S”	83	53
54	36	“1”	49	31
55	37	“1”	49	31
56	38	“0”	48	30
57	39	“0”	48	30
58	3A	“P”	80	50
59	3B	“E”	69	45
60	3C	“D”	68	44
61	3D	“-”	45	2D
62	3E	“3”	51	33
63	3F	“-”	45	2D
64	40	“0”	48	30
65	41	“0”	48	30
66	42	“1”	49	31
		PRODUCT NAME BYTES (15 Byte sequence)		
68	44	“D”	68	44
69	45	“S”	83	53
70	46	“1”	49	31
71	47	“1”	49	31
72	48	“0”	48	30
73	49	“0”	48	30
74	4A	“P”	80	50
75	4B	“E”	69	45
76	4C	“D”	68	44
77	4D	“-”	45	2D
78	4E	“3”	51	33
79	4F	“-”	45	2D
80	50	“0”	48	30
81	51	“0”	48	30
82	52	“1”	49	31
		PRODUCT SERIAL NUMBER BYTES		
		Model ID = DS1100PED-3-001 / K604		
87	57	"K“	75	4B
88	58	“6”	54	36
89	59	“0”	48	30
90	5A	“4”	52	34

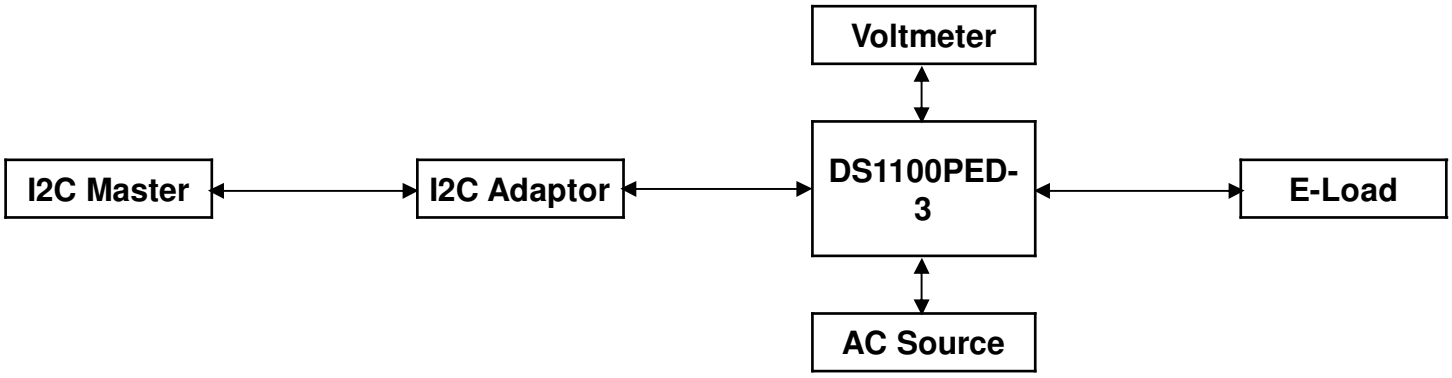
PMBus™ Interface Support

The DS1100PED-3 is compliant with the industry standard PMBus™ protocol for monitoring and control of the power supply via the I2C interface port.

DS1100PED-3 Series PMBus™ General Instructions

Equipment Setup

The following is typical I2C communication setup:



PMBus™ Writing Instructions

When writing to any PMBus™ R/W registers, ALWAYS do the following:

Disable Write Protect (command 10h) by writing any of the following accordingly:

Levels: 00h – Enable writing to all writeable commends

20h – Disables write except 10h, 01h, 00h, 02h and 21h commands

40h – Disables write except 10h, 01h, and 00h commends

80h – Disable write except 00h command

To save changes on the USER PMBus™ Table:

Use send byte command: 15h STORE_USER_ALL

To save changes on the DEFAULT PMBus™ Table:

Use send byte command: 11h STORE_DEFAULT_ALL

Wait for 5 seconds, turn-off the PSU, wait for another 5 seconds before turning it on.

DS1100PED-3 Series Support PMBus™ Command List

The DS1100PED-3 is compliant with the industry standard PMBus™ protocol for monitoring and control of the power supply via the I2C interface port.

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value	Access Type	Data Bytes	Data Format	Description
00h	PAGE	00	R	1	HEX	
01h	OPERATION	80	R/W	1	bitmapped	Used to Turn the unit ON/OFF in conjunction with the input PS_ON pin. It is also used to set output to upper or lower Margin Voltages. Valid values: 80h=PSU ON; 40h=PSU OFF.
	b7:6	10b				01 – Soft Turn OFF (With Sequencing) 10 – PSU ON
	b5:4	00b				
	b3:2	00b				
	b1:0	00b				Reserved
02h	ON_OFF_CONFIG	1C	R	1	bitmapped	Configures the combination of PS_ON pin and serial communication commands needed to turn the Unit ON/OFF.
	b7:5	000				Reserved
	b4 – Enable CONTROL pin and Serial communication control.	1				0 – Unit powers up any time power is present regardless of the state of CONTROL pin. 1 – Unit powers up as dictated by CONTROL pin and OPERATION command (b3:0)
	b3 – Serial communication Control	1				0 – Unit Ignores ON/OFF portion of the OPERATION command.1 – Enables Serial communication ON/OFF portion of OPERATION command. Requires CONTROL pin to be asserted for the unit to start and energize the output.
	b2 – Sets how the unit responds to CONTROL pin	1				0 – Unit ignores CONTROL pin. (ON/OFF controlled by OPERATION command). 1 – Unit requires CONTROL pin to be asserted to start the unit.
	b1 - CONTROL pin polarity	0				0 – Active Low (Pull Low to start the unit) 1 – Active high (Pull high to start the unit)
	b0 – CONTROL pin Action	0				0 – Use programmed turn ON/OFF delay 1 – Turn OFF the output and stop transferring energy to the output as fast as possible.
03h	CLEAR_FAULTS	0	S			
10h	WRITE_PROTECT	80	R/W	1	bitmapped	Used to Control Writing to the PMBus Device 80h - Disables write except 10h (write protected) 40h – Disables write except 10h, 01h, 00h 20h – Disables write except 10h,01h,00h,02h and 21h commands 00 – Enables write to all writeable commands.
12h	RESTORE_DEFAULT_ALL	-	S	0		Copies the entire contents of the DEFAULT non-volatile memory to the Operating memory table.
15h	STORE_USER_ALL	-	S	0		Copies the Operating memory table to the matching USER non-volatile memory.
16h	RESTORE_USER_ALL	-	S	0		Copies the entire USER non-volatile memory to the Operating memory table.
19h	CAPABILITY	90	R	1		Provides a way for the hosts system to determine some key capabilities of a PMBus device.
	b7 - Packet Error Checking	1				0 - PEC not supported 1 - PEC supported
	b6 - Maximum Bus Speed	0				0 - Maximum supported bus speed, 100khz 1 - Maximum supported bus speed, 400khz

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
	b5 - SMBALERT#	0				0 – SMBus Alert Pin *not supported* 1 – SMBus Alert Pin *supported*
	b4:0	00000				Reserved
1Ah	QUERY		R	1	bitmapped	
20h	VOUT_MODE	17	R	1		Specifies the mode and parameters of Output Voltage related Data Formats
21h	VOUT_COMMAND	1801	R/W	2	Linear	Sets the Output Voltage Reference Vout command sends discreet value to change or trim output voltage. The value acts as Digital reference of the Power supply after additional operations are performed. (to make the representation compatible). Affects OVP_WARNING and FAULT LIMIT, as well as POWER_GOOD_ON/OFF level. (Valid value range is 11.4V – 12.6V)
24h	VOUT_MAX	1933	R	2	Linear	Default value is 12.6V.
30h	COEFFICIENTS	FFFF	R	6		use to retrieve the m, b and R coefficients, needed for DIRECT data format
	byte 1:2					mlow Byte, m high byte
	byte 3:4					b low Byte, b high byte
	byte 5					R byte
35h	VIN_ON	EAC0	R	2	Linear	Sets the value of input, in volts, at which the unit should start. ACGOOD at 88Vac input
36h	VIN_OFF	EA98	R	2	Linear	Sets the value of input, in volts, at which the unit should stop power conversion. ACBAD at 83Vac input
3Ah	FAN_ CONFIG_1_2	90	R	1		Read only to reflect setting of Fans
	b7	1				1 – Fan is installed in position 1 0 – No Fan is installed in position 1
	b6	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC
	b5:4	01				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
	b3	0				1 – Fan is installed in position 2 0 – No Fan is installed in position 2
	b2	0				1 – Fan is commanded in RPM 0 – Fan is commanded in DC
	b1:0	00				00 – 1 pulse per revolution 01 – 2 pulses per revolution 10 – 3 pulses per revolution 11 – 4 pulses per revolution
3Bh	FAN_COMMAND_1	0000	R/W	2	Linear	Adjusts the operation of the Fans. The device can override the command, if it requires higher value, to maintain proper device temperature. Duty cycle Control – Commands Speeds from 0% to 100%
40h	VOUT_OV_FAULT_LIMIT	1C81	R/W	2	Linear	Sets Output Over voltage threshold. (Default Value is 14.25V) Valid value range is 13.5V – 15.0V.
41h	VOUT_OV_FAULT_RESPONSE	80	R	1		Unit Latches OFF. Resets on PS_ON or CONTROL pin recycle or AC recycle.
42h	VOUT_OV_WARN_LIMIT	1A01	R/W	2	Linear	Sets Over-voltage Warning threshold. (Default Value is 13.0V)

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
44h	VOUT_UV_FAULT_LIMIT	1599	R/W	2	Linear	Sets Under-voltage Fault threshold. (Default Value is 10.8V)
45h	VOUT_UV_FAULT_RESPONSE	80	R	1		Turn PSU OFF
46h	IOUT_OC_FAULT_LIMIT	EB60	R/W	2	Linear	Sets the Over current threshold in Amps. (Default Value is 108A)
47h	IOUT_OC_FAULT_RESPONSE	C0	R	1		OCP ride through. If OCP persists.
4Ah	IOUT_OC_WARN_LIMIT	EB20	R/W	2	Linear	Sets the Over Current Warning threshold in Amps. (Default value is 100A)
4Fh	OT_FAULT_LIMIT	EB48	R/W	2	Linear	Secondary ambient temperature Fault threshold, in degree C. (Default value is 105degC)
50h	OT_FAULT_RESPONSE	F8	R	1	Linear	Turn PSU OFF and will retry indefinitely. Supported enable/disable of protection and recoverability.
51h	OT_WARN_LIMIT	EB20	R/W	2	Linear	Secondary ambient temperature warning threshold, in degree C. Operating limit (Default value is 100degC)
55h	VIN_OV_FAULT_LIMIT	FA26	R	2	Linear	Sets input over-voltage threshold. (Default value is 275Vac)
56h	VIN_OV_FAULT_RESPONSE	C0	R	1		
58h	VIN_UV_WARN_LIMIT	EAB8	R	2	Linear	(Default value is 87Vac)
59h	VIN_UV_FAULT_LIMIT	EA80	R	2	Linear	(Default value is 80Vac)
5Ah	VIN_UV_FAULT_RESPONSE	F8	R	1		
5Eh	POWER_GOOD_ON	D2C0	R/W	2	Linear	Sets the threshold by which the Power Good signal is asserted. Default=11.0V
5Fh	POWER_GOOD_OFF	0000	R/W	2	Linear	Sets the threshold by which the Power Good signal is de-asserted. Default=10.8V
60h	TON_DELAY	9B33	R/W	2	Linear	Sets the time (sec), from start condition (Power ON) until the output starts to rise. (2.2sec max) Default=0.1s
61h	TON_RISE	8BD7	R/W	2	Linear	Sets the time (ms), for the output rises from 0 to regulation. (0-50ms) Default=30ms
62h	TON_MAX_FAULT_LIMIT	C233	R/W	2	Linear	(Valid value is 2000-2400ms) Default=2200ms
63h	TON_MAX_FAULT_RESPONSE	C0	R	1	Bitmapped	Default=C0h
64h	TOFF_DELAY	C280	R/W	2	Linear	Sets the time (ms), from a stop condition (Power OFF) until the output starts to drop (converter OFF).(2.7sec max) Default=2.5secs
6Ah	POUT_OP_WARN_LIMIT	0A78	R/W	2	Linear	(Valid value is 1263 – 1265W) Default= 1264W
78h	STATUS_BYTE	00	R	1		Returns the summary of critical faults
	b7 – BUSY					Not supported
	b6 – OFF					Unit is OFF
	b5 – VOUT_OV					Output over-voltage fault has occurred
	b4 – IOUT_OC					Output over-current fault has occurred
	b3 - VIN_UV					An input under--voltage fault has occurred
	b2 - TEMPERATURE					A temperature fault or warning has occurred
	b1 – CML					A communication, memory or logic fault has occurred.
	b0 – NONE OF THE ABOVE					A Fault Warning not listed in bits[7:1] has occurred.
79h	STATUS_WORD	00	R	1		Summary of units Fault and warning status.
	b15 – VOUT					An output voltage fault or warning has occurred
	b14 – IOUT/POUT					An Output current or power fault or warning has occurred.
	b13 – INPUT					An input voltage, current or power fault or warning as occurred.
	b12 – MFR					A manufacturer specific fault or warning has occurred.
	b11 – POWER_GOOD#					The POWER_GOOD signal is de-asserted
	b10 - FANS					A fan or airflow fault or warning has occurred.

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
	b9 – OTHER					Not supported
	b8 – UKNOWN					Not supported
	b7 – BUSY					A fault was declared because the device was busy and unable to respond.
	b6 – OFF					Unit is OFF
	b5 – VOUT_OV					Output over-voltage fault has occurred
	b4 – IOUT_OC					Output over-current fault has occurred
	b3 - VIN_UV					An input under-voltage fault has occurred
	b2 – TEMPERATURE					A temperature fault or warning has occurred
	b1 – CML					A communication, memory or logic fault has occurred.
	b0 – NONE_OF_THE_ABOVE					A fault or warning not listed in bits[7:1] of this byte has occurred.
7Ah	STATUS_VOUT	00	R	1		Output voltage related faults and warnings
	b7					VOUT Over--voltage Fault
	b6					VOUT Over-voltage warning
	b5					VOUT Under-voltage Warning
	b4					VOUT Under-voltage Fault
	b3					VOUT_MAX Warning, an attempt has been made to set output to a value higher that the highest permissible voltage.
	b2					TON_MAX_FAULT
	b1					TOFF_MAX Warning. Not supported
	b0					Not supported.
7Bh	STATUS_IOUT	00	R	1		Output Current related faults and warnings
	b7					IOUT Over current Fault
	b6					IOUT Over current And Low Voltage shutdown Fault
	b5					IOUT Overcurrent Warning
	b4					IOUT Undercurrent Fault
	b3					Current Share Fault
	b2					Power Limiting
	b1					POUT Overpower Fault
	b0					POUT Overpower Warning
7Ch	STATUS_INPUT	00	R	1		Input related faults and warnings
	b7					VIN Overvoltage Fault
	b6					VIN Overvoltage Warning
	b5					VIN Undervoltage Warning
	b4					VIN Undervoltage Fault
	b3					Unit is OFF for insufficient Input Voltage
	b2					IIN Overcurrent Fault
	b1					IIN Overcurrent Warning
	b0					PIN Overpower Warning
7Dh	STATUS_TEMPERATURE	00	R	1		Temperature related faults and warnings
	b7					Overtemperature Fault
	b6					Overtemperature Warning
	b5					Undertemperature Warning
	b4					Undertemperature Fault
	b3:0					Reserved

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
7Eh	STATUS_CML	00	R	1		Communications, Logic and Memory
	b7					Invalid or unsupported Command Received
	b6					Invalid Data
	b5					Packet Error Check Failed
	b4					Memory Fault Detect, CRC Error
	b3					Not Supported
	b2					Not Supported
	b1					Not Supported
	b0					Not Supported
81h	STATUS_FANS_1_2	00	R	1		
	b7					Fan 1 Fault
	b6					Fan 2 Fault
	b5					Fan 1 Warning
	b4					Fan 2 Warning
	b3					Fan_1 Speed Overridden
	b2					Fan_2 Speed Overridden
	b1					Not Used
	b0					Not Used
86h	READ_EIN	-	R	2	Linear	Returns the accumulated input power over time
87h	READ_EOUT	-	R	2	Linear	Returns the accumulated output power over time
88h	READ_VIN	-	R	2	Linear	Returns input Voltage in Volts AC.
89h	READ_IIN	-	R	2	Linear	Returns input Current in Amperes
8Ah	READ_VCAP	-	R	2	Linear	Returns Bulk Capacitor voltage in Volts
8Bh	READ_VOUT	-	R	2	Direct	Returns the actual, measured voltage in Volts.
8Ch	READ_IOUT	-	R	2	Linear	Returns the output current in Amperes.
8Dh	READ_TEMPERATURE_1	-	R	2	Linear	Secondary Hotspot
8Eh	READ_TEMPERATURE_2	-	R	2	Linear	Secondary Ambient
8Fh	READ_TEMPERATURE_3	-	R	2	Linear	Primary Ambient
90h	READ_FAN_SPEED_1	-	R	2	Linear	Speed of Fan 1
96h	READ_POUT	-	R	2	Linear	Returns the output power, in Watts.
97h	READ_PIN	-	R	2	Linear	Returns the input power, in Watts.
98h	PMBUS_REVISION	22	R	1		Reads the PMBus revision number
	b7:5	0001				Part 1 Revision 0000 – Revision 1.0 0001 – Revision 1.1
	b4:0	0001				Part 2 Revision 0000 – Revision 1.0 0001 – Revision 1.1
99h	MFR_ID	“ALL”	BR, ASCII	7		Abbrev or symbol of manufacturers name. ASCII (EMERSON)
9Ah	MFR_MODEL	“DS1100PED-3”	BR, ASCII	11		Manufacturers Model number, ASCII format
9Bh	MFR_REVISION	4110	BR, ASCII	2		Manufacturers, revision number, ASCII format
9Ch	MFR_LOCATION	“xxxxxxxxxxxx”	BR, ASCII			Manufacturers facility, ASCII format
9Dh	MFR_DATE	“xxxxxx”	BR	6		Manufacture Date, ASCII format structure : YYMMDD
9Eh	MFR_SERIAL	“xxxxxxxxxxxxxxx”	BR	13		Unit serial number, ASCII format.

DS1100PED-3 Series Supported PMBus™ Command List:

Command Code	Command Name	Default Value (HEX)	Access Type	Data Bytes	Data Format	
A0h	MFR_VIN_MIN	EAD0	R	2	Linear	Minimum Input Voltage (Default value is 90Vac)
A1h	MFR_VIN_MAX	FA10	R	2	Linear	Maximum Input Voltage (Default value is 264Vac)
A2h	MFR_IIN_MAX	D380	R	2	Linear	Maximum Input Current (Default value is 14.5A)
A3h	MFR_PIN_MAX	0A76	R	2	Linear	Maximum Input Power (Default value is 1260W)
A4h	MFR_VOUT_MIN	16CD	R	2	Linear	Minimum Output Voltage Regulation Window. (Default value is 11.4V)
A5h	MFR_VOUT_MAX	1933	R	2	Linear	Maximum Output Voltage. Regulation Window (Default value is 12.6V)
A6h	MFR_IOUT_MAX	EADD	R	2	Linear	Maximum Output Current (Default value is 91.6A)
A7h	MFR_POUT_MAX	0A26	R	2	Linear	Maximum Output Power (Default value is 1100W)
A8h	MFR_TAMBIENT_MAX	E320	R	2	Linear	Maximum Operating Ambient Temperature (Secondary Ambient) (Default value is 50 degC)
A9h	MFR_TAMBIENT_MIN	0000	R	2	Linear	Minimum Operating Ambient Temperature (Secondary Ambient) (Default value is 0 degC)
AAh	MFR_EFFICIENCY_LL		R	14		
ABh	MFR_EFFICIENCY_HL		R	14		
B0h	USER_DATA_00		R/W			
E0h	FW_PRI_VERSION		R	8	ASCII	
E1h	FW_SEC_VERSION		R	8	ASCII	
E2h	CONFIG_UNLOCK_CODE		R/W	4		
E3h	CONFIG_CTRL_CMD		R/W			
F1h	ISP_UNLOCK_CODE		R/W	4		
F2h	ISP_CTRL_CMD		R/W			
F3h	ISP_STATUS_BYTE		R			
F5h	ISP_FLASH_DATA		R/W	16		

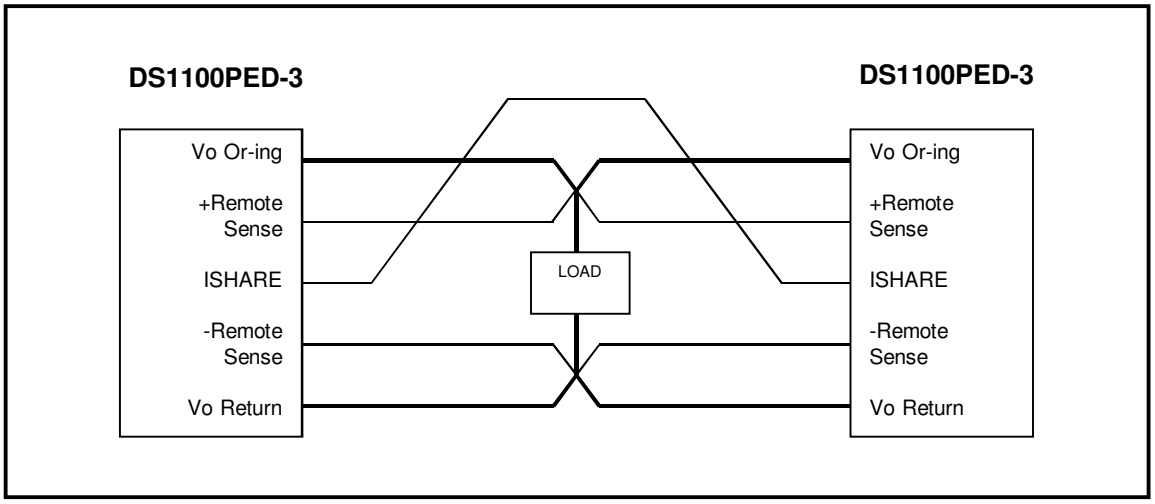
Current Sharing

The DS1100PED-3 series' main output V_O is equipped with current sharing capability. This will allow up to 6 power supplies to be connected in parallel for higher power application. Current share accuracy is typical with 3.7A at full load. When supplying light loads between 20% to 100% of its rated load, the power supplies will share within 3.7A accuracy. Below 20% total loading, there is no guarantee of output current sharing.

Redundancy / Fault Tolerance

The DS1100PED-3 series power supplies must be able to current share with 2(1+1) up to 4(2+2) or 6(3+3) power supplies in parallel and operate in a hot swap/redundant N+N configuration where N=1, 2, or 3. The 12Vsb outputs of the power supplies are connected together in the system so that a failure or hot swap of a redundant power supply does not cause these outputs to go out of regulation in the system.

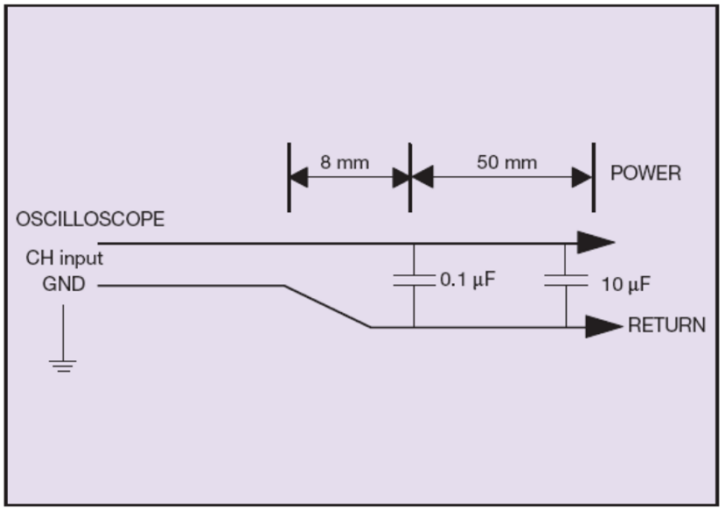
All power supply outputs will be designed for redundant mode operation. No internal failure in any power supply in this configuration should cause the bus voltage to fall below the regulation limits specified. All output voltages should stay within the regulation limits during cold swapping or hot swapping operation.



Note - For ease of current sharing, it is recommended that the remote sense lines be connected right at the mating connector of each power supply. Current sharing tests should be done with a backplane distribution impedance of 200uohm.

Output Ripple and Noise Measurement

The setup outlined in the diagram below has been used for output voltage ripple and noise measurements on the DS1100PED-3 Series. When measuring output ripple and noise, a scope jack in parallel with a 0.1uF ceramic chip capacitor, and a 10uF aluminum electrolytic capacitor should be used. Oscilloscope should be set to 20 MHz bandwidth for this measurement.



Record of Revision and Changes

Issue	Date	Description	Originators
1.0	07.21.2014	First issue	D. Hou
1.1	08.20.2015	Update the address bits	D. Hou
1.2	10.30.2015	Update PS_ON_L and PS PRESENT description / Update the command code 8Dh,8Eh,8Fh description	D. Hou
1.3	05.06.2016	Update Vo Current Share Accuracy / Add the China CCC Approval Exemption / Update ISHARE description	D. Hou
1.4	06.14.2017	Update the 'FAN_COMMAND_1' description	D. Hou
1.5	10.11.2017	Delete '00 – Immediate Turn OFF (No Sequencing)' of 01h command. Update the LED status of standby OCP from 'Blinking Amber' to 'Blinking Green'. Update command code 86h description. And update some typo issue.	D. Hou
1.6	02.23.2018	Update diagram for PS_ON# signal.	D. Hou

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